

SUPER○[®]

SUPER○[®]X6DHP-TG

USER'S MANUAL

Revision 1.0a

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Preface

About This Manual

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the **SUPER**® X6DHP-TG motherboard. The X6DHP-TG supports single or dual Intel® Xeon E64MT (Nocona™) processors at a 800 MHz front side bus. Based upon Intel's NetBurst microarchitecture, the Xeon E64MT (Nocona) processor supports the IA-32 software and includes features found in the Xeon™ processor such as Hyper Pipelined Technology, allowing the processor to operate at much higher core frequencies. Packaged in a 604-pin Flip Chip Micro Pin Grid Array(FC-mPGA4) platform in a Zero Insertion Force (ZIF) socket (mPGA 604), the Xeon E64MT Processor (800 MHz) is ideal for high performance workstation and server environments with up to two processors on one system bus. The **SUPER**® X6DHP-TG motherboard is intended to be professionally installed.

Manual Organization

Chapter 1 begins with a checklist of what should be included in your mainboard box, describes the features, specifications and performance of the motherboard and provides detailed information about the chipset.

Chapter 2 begins with instructions on handling static-sensitive devices. Read this chapter when you want to install the processor and DIMM memory modules and when mounting the mainboard in the chassis. Also refer to this chapter to connect the floppy and hard disk drives, SCSI drives, the IDE interfaces, the parallel and serial ports, the keyboard and mouse, the power supply and various control panel buttons and indicators.

If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for the video, the memory and the setup configuration stored in CMOS. For quick reference, a general FAQ [Frequently Asked Questions] section is provided.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the CMOS Setup utility.

Appendix A provides BIOS POST codes.

Appendix B provides software and the OS installation instructions.

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Chapter 1

Introduction

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance. Check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

- One (1) Supermicro Mainboard
- One (1) ribbon cable for IDE devices (CBL-036)
- One (1) floppy ribbon cable (CBL-022)
- One (1) User's/BIOS Manual

Contacting Supermicro

Headquarters

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980 Rock Ave.
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Web Site: www.supermicro.com.tw

Technical Support:

Email: support@supermicro.com.tw
Tel: 886-2-8228-1366, ext.132 or 139

Figure 1-1. **SUPER[®] X6DHP-TG Image**

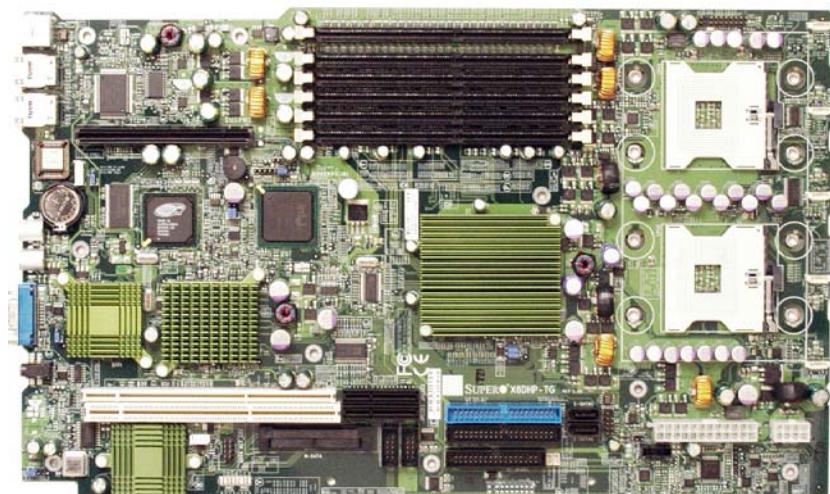
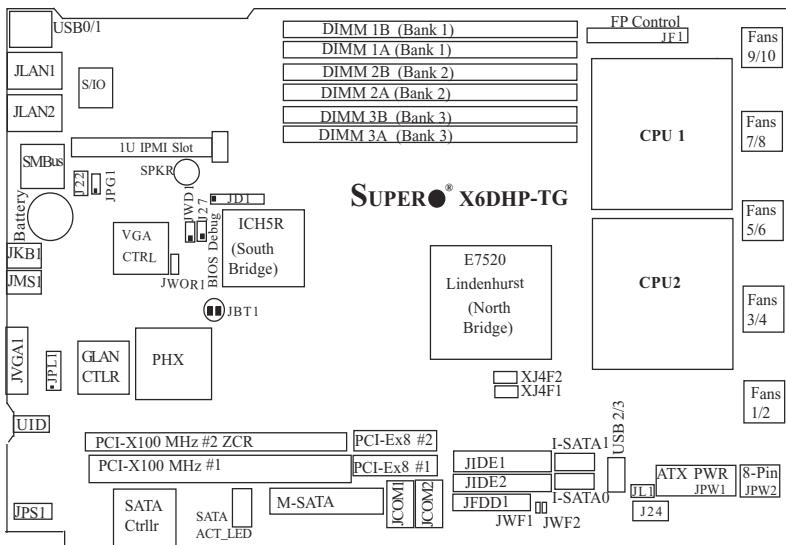


Figure 1-2. SUPER[®] X6DHP-TG Motherboard Layout

(not drawn to scale)



Notes:

1. Jumpers not indicated are for testing only.
2. See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
3. "■" indicates the location of Pin 1.
4. The graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of this manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.

Quick Reference (X6DHP-TG)

(*Please refer to Chapter 2 for pin definitions and detailed information.)

<u>Jumper</u>	<u>Description</u>	<u>Default Setting</u>
GBT1	CMOS Clear	See Chapter 2
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPL1	GLAN Enable	Pins 1-2 (Enabled)
JPS1	SATA Enable	Pins 1-2 (Enabled)
JWD1	Watch Dog Enable	Pins 1-2 (Reset)
XJ4F1/XJ4F2	PLLSEL (Memory Speed Select)	Closed (333MHz)
	<u>Connector</u>	<u>Description</u>
1U IPMI	1U IPMI slot	
DIMM#1A-DIMM#3B	DDR DIMM Memory (RAM) Slots	
FAN 1-10	CPU/Chassis/Overheat Fan Headers	
J22	System Management Bus Connector	
J24	Power System Management Bus Connector	
J27	BIOS Debug	
JCOM1/JCOM2	COM1/COM2 Serial Port Connectors	
JD1	PWR LED(pins1-3)/SpeakerHeader(pins 4-7)	
JF1	Front Control Panel Connector	
JFDD1	Floppy Disk Drive Connector	
JWF1/JWF2 (Note1)	DOM Bus Select (JWF1: Master, JWF2: Slave)	
JIDE1J/IDE2	IDE#1/IDE#2 Hard Disk Drive Connectors	
JKB1/JMS1	PS2 Keyboard Connector (JKB1)/Mouse (JMS1)	
JL1	Chassis Intrusion Header	
JLAN 1/2	G-bit Ethernet (GLAN) Ports 1/2	
JPWR1	Primary 24-Pin ATX PWR Connector	
JPWR2	12V 8-Pin CPU PWR Connector	
JOH1	Overheat LED Header	
JVGA1	Video Connector	
JWOR1(WOR)	Wake-on-Ring Header	
PCI-X#1/PCI-X#2	PCI-X 100MHz Slot1/PCI-X 100MHz ZCR (Slim)Slot2	
PCI-E#1/PCI-E#2	PCI-Express x8 Slot1/Slot2	
I-SATA 0/1	Intel ICH5R Serial ATA0/1 Ports	
M-SATA	Marvell Serial ATA 4-port Connections	
SATA ACT_LED	Marvell Serial ATA LED Activity Indicator	
SMBus	System Management Bus	
UID	Unit Identification Button	
USB 0/1	Back Panel USB0/USB1 Headers	
USB 2/3	Front Panel Universal Serial Bus USB2/USB3 Ports	

Note1: For OEM Use only

Motherboard Features

CPU

- Single or dual Intel® 604-pin Xeon EMT64 (Nocona™) processors at 800 MHz front side (system) bus speed.

Memory

- Six 184-pin DIMM sockets supporting up to 24 GB (FOR DDR 266) or 12 GB (FOR DDR 333) Registered ECC DDR 266/333 (PC 2100/PC 2700) SDRAM

Chipset

- Intel 7520 Lindenhurst chipset

Expansion Slots

- One PCI-X 100MHz ZCR slot (Slot#2), One PCI-X 100MHz slot (Slot#1)
- Two PCI-Express x8 slots

BIOS

- 8 Mb Phoenix® Flash ROM
- DMI 2.3, PCI 2.2, ACPI 1.0, Plug and Play (PnP), SMBIOS 2.3

PC Health Monitoring

- Onboard voltage monitors for CPU cores, chipset voltage, Memory Voltage, 3.3V, +5V, +12V, -12V and 3.3V Standby
- Fan status monitor with BIOS control
- CPU slow-down on temperature overheat
- CPU thermal trip support for processor protection
- Power-up mode control for recovery from AC power loss
- Auto-switching voltage regulator for CPU core
- System overheat/Fan Fail LED and control
- Chassis intrusion detection

ACPI Features

- Microsoft OnNow
- Slow blinking LED for suspend state indicator
- Main switch override mechanism

Onboard I/O

- One 1U IPMI slot
- Intel 82546GB Gigabit Ethernet controller support two GLAN ports

- 2 EIDE Ultra DMA/100 bus master interfaces
- 1 floppy port interface
- PS/2 mouse and PS/2 keyboard ports
- Up to 4 USB 2.0 (Universal Serial Bus) (2 ports, 2 Headers)
- Super I/O
- 2 SATA ports support 2 drives (RAID 0, 1, JBOD)
- ATI 8 MB Rage XL Graphic Card
- 2 COM Headers

Other

- Internal/external modem ring-on
- Console redirection
- SMBus for SMC Power Supply

CD/Diskette Utilities

- BIOS flash upgrade utility and device drivers

Dimensions

- ATX Ext. 9.6" x 16.2" (243.8 x 411.5 mm)

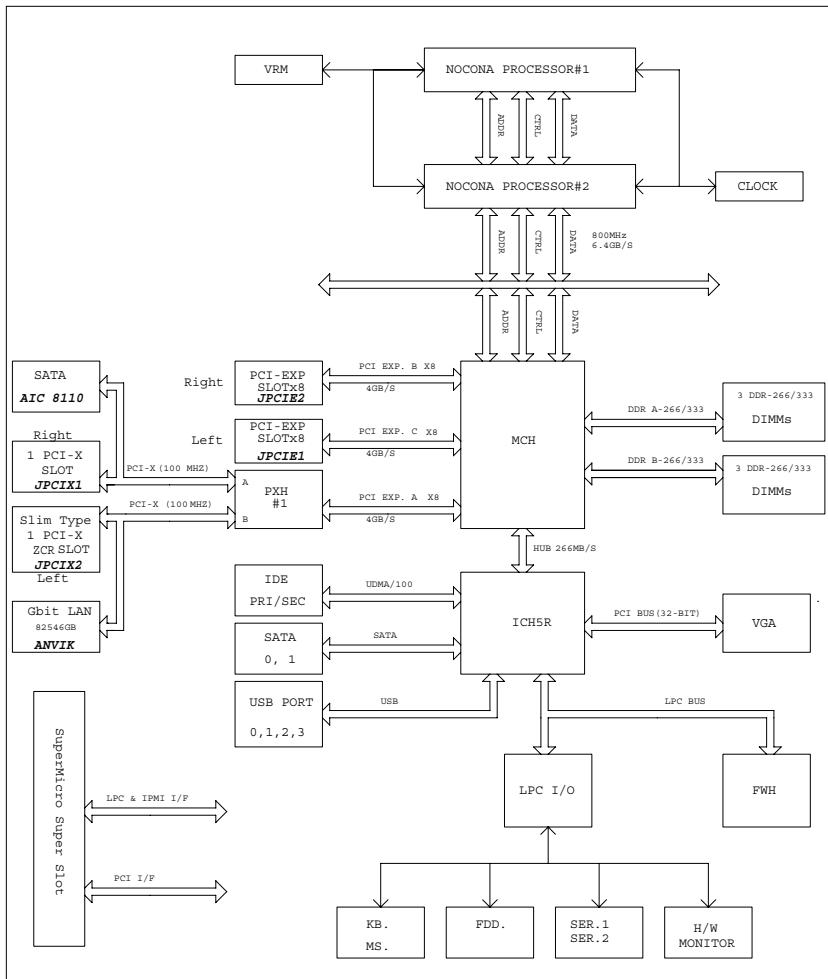


Figure 1-9. Block Diagram of the E7520 (Lindenhurst) Chipset

Note: This is a general block diagram. Please see the previous Motherboard Features pages for details on the features of the motherboard.

1-2 Chipset Overview

Built upon the functionality and the capability of the 7520 Lindenhurst chipset, the X6DHP-TG motherboard provides the performance and feature set required for dual processor-based servers, with configuration options optimized for communications, presentation, storage, computation or database applications. The Intel E7520 (Lindenhurst) chipset consists of the following components: the Lindenhurst Memory Controller Hub (MCH), the ICH5R I/O Controller Hub, and the Intel PCI-X Hub (PXH).

The E7520 MCH supports single or dual Xeon EMT64 (Nocona) processors with Front Side Bus speeds of 800 MHz. Its memory controller provides direct connection to two channels of registered DDR-II 400 with a matched system bus address and data bandwidths. The E7520 also supports the new PCI high speed serial I/O interface for superior I/O bandwidth. The MCH interfaces with the ICH5R I/O Controller Hub (ICH5R) via a dedicated Hub Interface. The PXH provides connection between a PCI interface and two independent PCI bus interfaces that can be configured for standard PCI-X 1.0 protocol.

ICH5R System Features

In addition to providing the I/O subsystem with access to the rest of the system, the ICH5R I/O Controller Hub integrates many I/O functions.

The ICH5R I/O Controller Hub integrates: 2-channel Ultra ATA/100 Bus Master IDE Controller, two Serial ATA (SATA) Host w/RAID0, RAID1 support, SMBus 2.0 Controller, LPC/Flash BIOS Interface, PCI 2.2 Interface and System Management Controller.

1-3 Special Features

Recovery from AC Power Loss

BIOS provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must hit the power switch to turn it back on) or for it to automatically return to a power-on state. See the Power Lost Control setting in the Advanced BIOS Setup section (within "Boot Features") to change this setting. The default setting is Always Off.

1-4 PC Health Monitoring

This section describes the PC health monitoring features of the SUPER X6DHP-TG. All have an onboard System Hardware Monitor chip that supports PC health monitoring.

Onboard Voltage Monitors for the CPU Cores, Chipset Voltage, Memory Voltage, +3.3V, +5V, +12V, -12V and +3V standby .

An onboard voltage monitor will scan these voltages continuously. When you use Supero Doctor II or Supero Doctor III, a warning is given or an error message is sent to the screen if a voltage becomes unstable. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor via SDIII.

Fan Status Monitor with Speed Control by BIOS

The PC health monitor can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by Thermal Management via BIOS.

CPU Fan Auto-Off in Sleep Mode

The CPU fan activates when the power is turned on. It continues to operate when the system enters Standby mode. When in sleep mode, the CPU will not run at full power, thereby generating less heat.

CPU Overheat LED and Control

This feature is available when the user enables the CPU overheat warning function in the BIOS. This allows the user to define an overheat temperature. When this temperature is exceeded, both the overheat fan and the warning LED are triggered.

System Resource Alert

This feature is available when used with Supero Doctor III in the Windows OS environment. SDIII is used to notify the user of certain system events. For example, if the system is running low on virtual memory and there is insufficient hard drive space for saving the data, you can be alerted of the potential problem.

Auto-Switching Voltage Regulator for the CPU Core

The auto-switching voltage regulator for the CPU core can support up to 20A current and auto-sense voltage IDs ranging from 0.83V to 1.63V. This will allow the regulator to run cooler and thus make the system more stable.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers. This also includes consumer devices connected to the PC such as VCRs, TVs, telephones and stereos.

In addition to enabling operating system-directed power management, ACPI provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with both Windows 2000, Windows 2003, and Windows NT 5.0.

Microsoft OnNow

The OnNow design initiative is a comprehensive, system-wide approach to system and device power control. OnNow is a term for a PC that is always on but appears to be off and responds immediately to user or other requests.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will wake-up and the LED will automatically stop blinking and remain on.

Main Switch Override Mechanism

When an ATX power supply is used, the power button can function as a system suspend button to make the system enter a SoftOff state. The monitor will be suspended and the hard drive will spin down. Depressing the power button again will cause the whole system to wake-up. During the SoftOff state, the ATX power supply provides power to keep the required circuitry in the system alive. In case the system malfunctions and you want to turn off the power, just depress and hold the power button for 4 seconds. This option can be set in the Power section of the BIOS Setup routine.

External Modem Ring-On

Wake-up events can be triggered by a device such as the external modem ringing when the system is in the SoftOff state. Note that external modem ring-on can only be used with an ATX 2.01 (or above) compliant power supply.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

The SUPER X6DHP-TG accommodates ATX 24-pin power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. You should use one that will supply at least 200W of power. Your power supply must supply 1.5A for the Ethernet ports. In addition, you should also use the onboard 12V 8-pin power

connector (JPW2) to support Intel Xeon CPUs. Failure to provide this extra power will result in instability of the CPU after a few minutes of operation. See Section 2-5 for details on connecting the power supply.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant (info at <http://www.ssiforum.org/>). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1-7 Super I/O

The disk drive adapter functions of the Super I/O chip include a floppy disk drive controller that is compatible with industry standard 82077/765, a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic. The wide range of functions integrated onto the Super I/O greatly reduces the number of components required for interfacing with floppy disk drives. The Super I/O supports 360 K, 720 K, 1.2 M, 1.44 M or 2.88 M disk drives and data transfer rates of 250 Kb/s, 500 Kb/s or 1 Mb/s. It also provides two high-speed, 16550 compatible serial communication ports (UARTs), one of which supports serial infrared communication. Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O supports one PC-compatible printer port (SPP), Bi-directional Printer Port (BPP) , Enhanced Parallel Port (EPP) or Extended Capabilities Port (ECP).

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.

The IRQs, DMAs and I/O space resources of the Super I/O can flexibly adjust to meet ISA PnP requirements, which support ACPI and APM (Advanced Power Management).

Notes

Chapter 2

Installation

2-1 Static-Sensitive Devices

Electric-Static-Discharge (ESD) can damage electronic components. To prevent damage to your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the anti-static bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery. Do not install the onboard upside down battery to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

2-2 Xeon EM64T (Nocona) Processor and Heatsink Installation



When handling the processor package, avoid placing direct pressure on the label area of the fan. Also, do not place the motherboard on a conductive surface, which can damage the BIOS battery and prevent the system from booting up.

IMPORTANT: Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket **before** you install the CPU heat sink.

CPU Installation

1. Lift the lever on the CPU socket:
lift the lever completely as shown on the picture on the right; otherwise, you will damage the CPU socket when you turn on the power. (Install CPU1 first.)



Socket lever

2. Insert the CPU in the socket and make sure that pin 1 of the CPU aligns with pin 1 of the socket (pin 1 is the corner marked with a triangle). When using only one CPU, install it into CPU socket #1 (socket #2 is automatically disabled if only one CPU is used).



Pin 1

3. Press the lever down until you hear a click to make sure that the CPU is securely installed in the CPU socket.



Socket lever in the locking Position

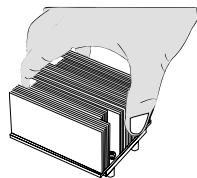
CEK Heatsink Installation

(The CEK Heatsink is heavy; please handle with care.)

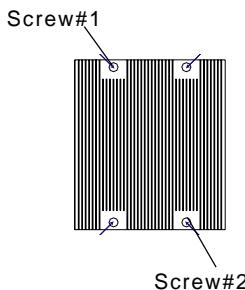
1. Do not apply any thermal compound to the heatsink or the CPU die-the required amount has already been applied.



2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.



3. Screw in two diagonal screws (ie the #1 and the #2 screws) until just snug (-do not fully tighten the screws to avoid possible damage to the CPU.)

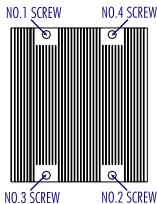


4. Finish the installation by fully tightening all four screws.

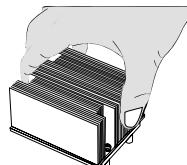
To Un-install the Heatsink

(Caution! We do not recommend that the CPU or the heatsink be removed. However, if you do need to un-install the heatsink, please follow the instructions below to uninstall the heatsink to prevent damage done to the CPU or the CPU socket.)

1. Unscrew and remove the heatsink screws from the motherboard in the sequence as show in the picture on the right.

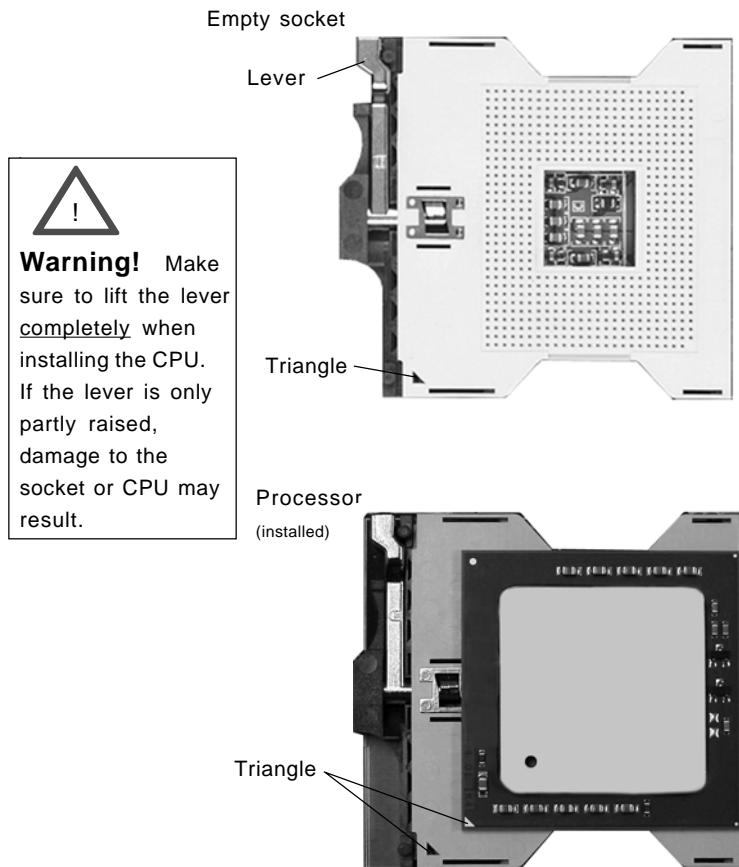


2. Hold the heatsink as show in the picture on the right and gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!!)



3. Once the CPU is loosened, remove the heatsink from the CPU socket.

4. Clean the surface of the CPU and the heatsink to get rid of the old thermal grease. Re-apply the proper amount of thermal grease on the surface before you re-install the CPU and the heatsink.

Figure 2-1. Processor Socket: Empty and with Processor Installed

Mounting the Motherboard in the Chassis

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then, use a screwdriver to secure the motherboard onto the motherboard tray. (*Note: some components are very close to the mounting holes. Please take precautionary measures to prevent damage done to these components when you install the motherboard to the chassis.)

2-3 Installing DIMMs

(*Note: Check the Supermicro web site for recommended memory modules:
http://www.supermicro.com/TECHSUPPORT/FAQs/Memory_vendors.htm)

CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage. Also note that the memory is interleaved to improve performance (see step 1).

DIMM Installation (See Figure 2-2 on Page 2-7)

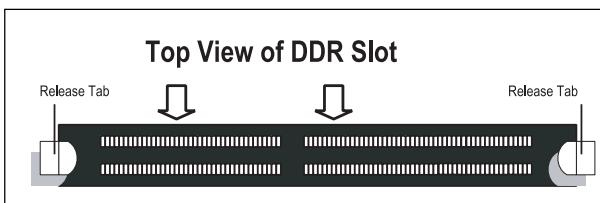
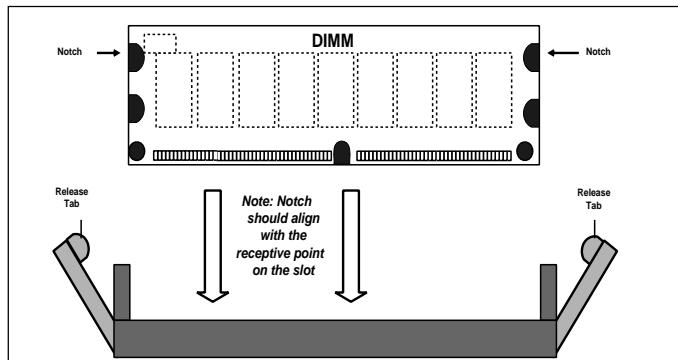
1. Insert the desired number of DIMMs into the memory slots, starting with Bank 1. The memory scheme is interleaved so you must install two modules at a time, beginning with Bank 1, then Bank 2, and so on.
2. Insert each DIMM module vertically into its slot. Pay attention to the notch along the bottom of the module to prevent inserting the DIMM module incorrectly.
3. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules (see step 1 above).

Memory Support

The X6DHP-TG supports up to 12 GB DDR-333 (PC 2700) or up to 24 GB DDR-266 (PC 2100) Registered ECC memory. All motherboards were designed to support 216 MB, 512 MB, 1 GB, 2 GB, 4 GB modules in each slot, but has only been verified for up to 2 GB modules. In order to support up to 6 DIMMs, single Rank memory must be used; otherwise, it can only support up to 4 DIMMs.

Figure 2-2. Installing and Removing DIMMs**To Install:**

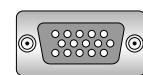
Insert module vertically and press down until it snaps into place. Pay attention to the alignment notch at the bottom.

**To Remove:**

Use your thumbs to gently push near the edge of both ends of the module. This should release it from the slot.

2-4 I/O Ports/Control Panel Connectors

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 2-3 below for the colors and locations of the various I/O ports.

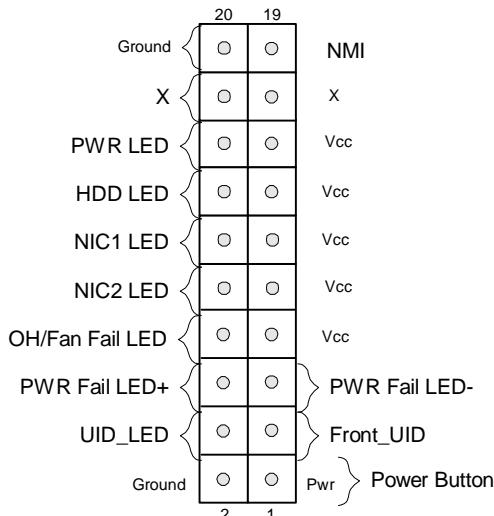
Figure 2-3. I/O Port Locations and DefinitionsUSB0/USB1
PortsLAN1
PortLAN2
PortKeyboard
(Purple)Mouse
(Green)VGA Graphics
Port (Blue)

UID

Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro server chassis. See Figure 2-4 for the descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.

Figure 2-4. JF1 Header Pins



2-5 Connecting Cables

ATX Power Connector

The main power supply connector (JPW1) on the X6DHP-TG meets the SSI (Superset ATX) specification. You can only use a 24-pin power supply cable on the motherboard. Make sure that the orientation of the connector is correct.

ATX Power Supply 24-pin Connector Pin Definitions(JPW1)

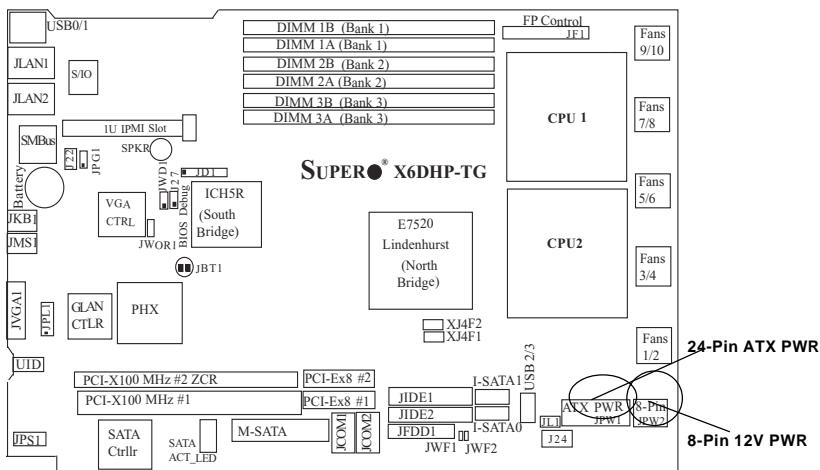
Pin Number	Definition	Pin Number	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON#	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res(NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Processor Power Connector

In addition to the Primary ATX power connector (above), the 12V 8-pin Processor connector at JPW2 must also be connected to your power supply for CPU power consumption to avoid causing instability to the system.

8-Pin +12v Power Supply Connector (JPW2)

Pins	Definition
1 thru 4	Ground
5 thru 8	+12v



NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)

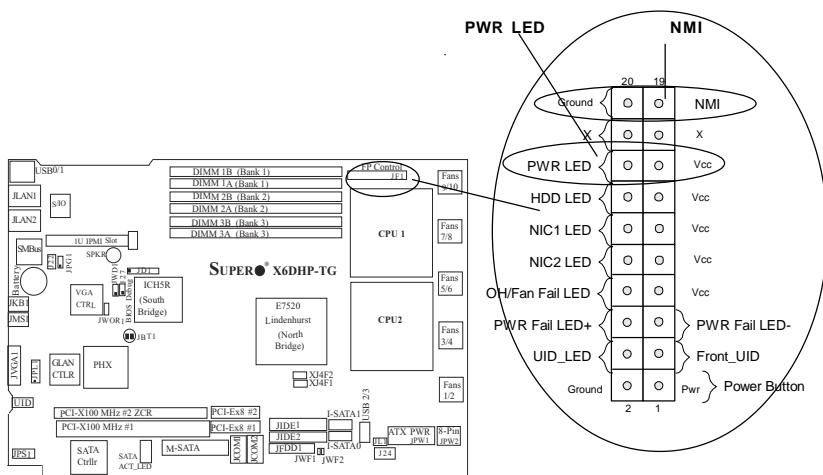
Pin Number	Definition
19	Control
20	Ground

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

PWR_LED Pin Definitions (JF1)

Pin Number	Definition
15	Vcc
16	Control



HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach the hard drive LED cable here to display disk activity (for any hard drives on the system, including SCSI, Serial ATA and IDE). See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)

Pin Number	Definition
13	Vcc
14	HD Active

NIC1/NIC2 LED Indicators

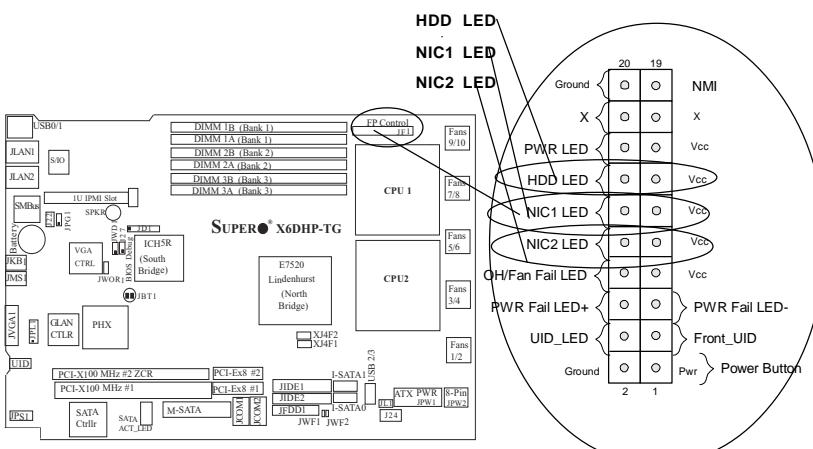
The NIC (Network Interface Controller) LED connections for the GLAN port1 is located on pins 11 and 12 of JF1, and for the GLAN port2 is located on pins 9 and 10 of JF1. Attach the NIC LED cables to display network activity. Refer to the tables on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)

Pin Number	Definition
11	Vcc
12	GND

NIC2 LED Pin Definitions (JF1)

Pin Number	Definition
9	Vcc
10	GND



Overheat/Fan_Fail LED

Connect an LED to the OH/Fan Fail connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating or system fan failure. Refer to the table on the right for pin definitions.

Overheat (OH)/
Fan_Fail LED Pin
Definitions
(JF1)

Pin Number	Definition
7	Vcc
8	GND

OH/Fan Fail LED
(JF1)

State	Message
Off	Normal
Stay On	Overheat
Blink	Fan Fail

PWR Fail LED

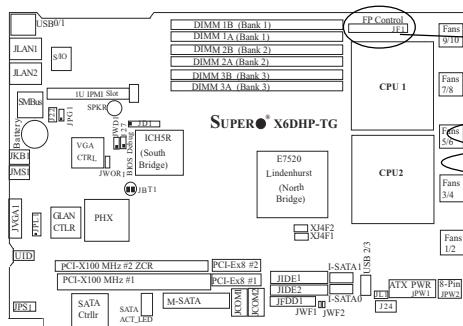
The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the tables on the right for pin definitions and color messages.

PWR_Fail LED
Pin Definitions
(JF1)

Pin Number	Definition
5	PWR Fail LED -
6	PWR Fail LED +

PWR_Fail LED Pin
Definitions
(JF1)

Color	System State
Green	PWR On, System Normal
Amber	Redundant PWR Failure

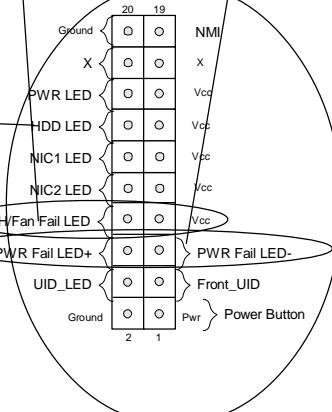


PW/PWR

OH / Fan

Fail LED

Fail LED



Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in BIOS - see Chapter 4). To turn off the power when set to suspend mode, depress the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Power Button Connector Pin Definitions (JF1)

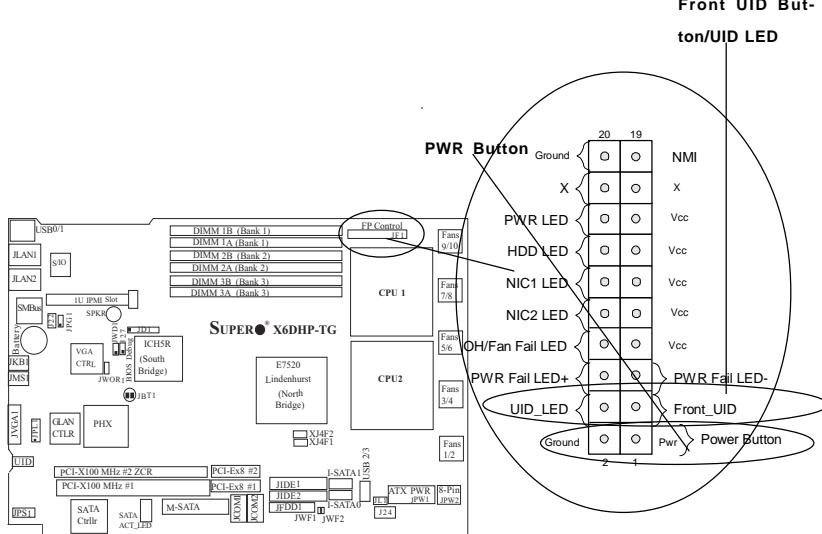
Pin Number	Definition
1	PW_ON
2	Ground

Front_UID Button/ UID_LED

There is a Universal Indicator located next to the VGA Connector on the motherboard. The Front_UID Button/UID_LED connection is located on pins 3 and 4 of JF1. Refer to the table on the right for pin definitions.

UID & UID_LED Pin Definitions (JF1)

Pin Number	Definition
3	Front_UID Button
4	UID_LED



Chassis Intrusion

A Chassis Intrusion header is located at JL1. Attach the appropriate cable to inform you of a chassis intrusion.

Chassis Intrusion Pin Definitions (JL1)

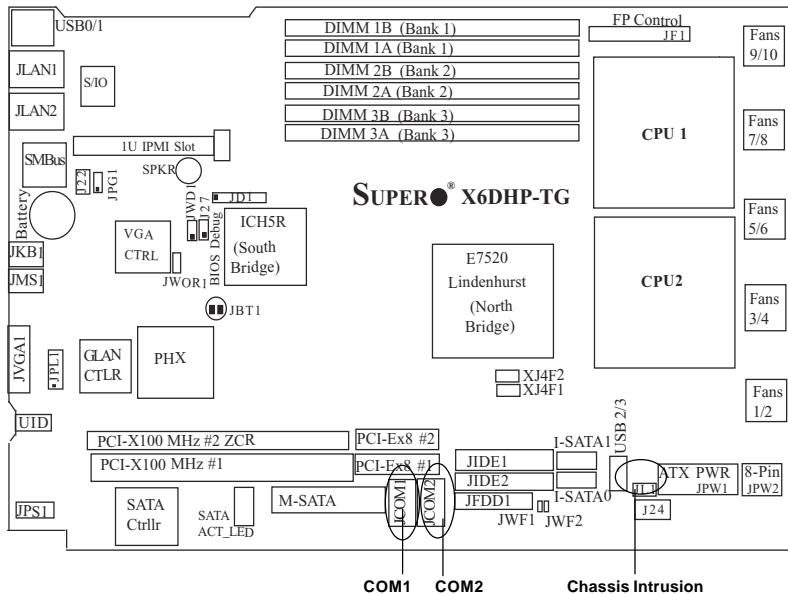
Pin Number	Definition
1	Intrusion Input
2	Ground

Serial Ports

There are two COM headers (COM1 and COM2) located between SCSI Channel A and IDE Drives. See the table on the right for pin definitions.

Serial Port Pin Definitions (COM1/COM2)

(COM17/COM2)			
Pin Number	Definition	Pin Number	Definition
1	CD	6	DSR
2	RD	7	RTS
3	TD	8	CTS
4	DTR	9	RI
5	Ground		



Universal Serial Bus (USB0/1)

Two USB 2.0 ports (USB0/USB1) are located on the Back Panel. See the table on the right for pin definitions.

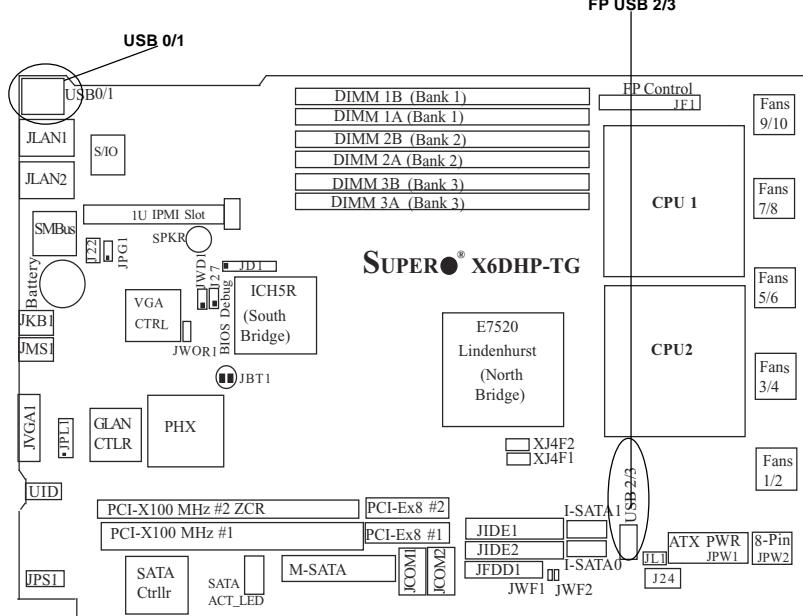
Universal Serial Bus Pin Definitions			
USB0		USB1	
Pin Number	Definition	Pin Number	Definition
1	+5V	1	+5V
2	P0-	2	P0-
3	P0+	3	P0+
4	Ground	4	Ground
5	N/A	5	Key

Front Panel Universal Serial Bus Headers

Extra USB headers (USB2/USB3) can be used for front side USB access. You will need a USB cable to use either connection. Refer to the tables on the right for pin definitions.

FP USB Pin Definitions

Pin Number	Definition	Pin Number	Definition
1	Vcc	2	Vcc
3	P0-	4	P0-
5	P0+	6	P0+
7	Ground	8	Ground
9	Key	10	NC



GLAN (Giga-bit Ethernet Ports)



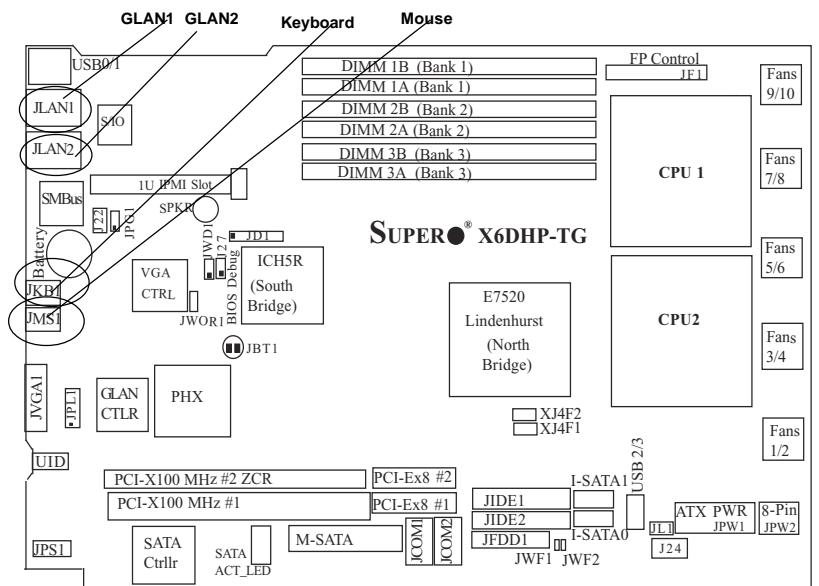
Two G-bit Ethernet ports (designated JLAN1/JLAN2) are located next to the USB 0/1 ports. This port accepts RJ45 type cables.

ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and PS/2 mouse are located next to the VGA connector. See the table at right for pin definitions. (See Figure 2-3 for the locations of each.)

PS/2 Keyboard and Mouse Ports Pin Definitions (JKB1/JMS1)

Pin Number	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC



Fan Headers

The X6DHP-TG has ten fan headers (Fan1 to Fan10). These fan headers use DC Power. **Note:** please be sure to use Supermicro's (P/N: Fan-0079) on the motherboard. See the table on the right for pin definitions.

Fan Header Pin Definitions

Definition	Pin #	Color
Fan PWR	1	Red
Tachometer	2	Yellow
GND	3	Black
GND	4	Grey
Tachometer	5	White
Fan PWR	6	Orange

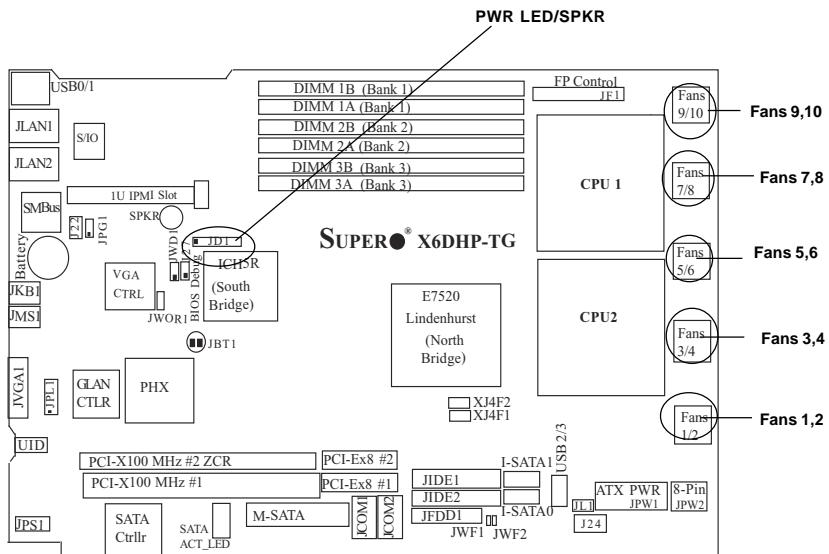
*Be sure to use Supermicro (P/N: Fan-0079) on the X6DHP-TG.

Power LED/Speaker

On the JDI header, pins 1-3 are for a power LED and pins 4-7 are for the speaker. See the table on the right for speaker pin definitions. **Note:** The speaker connector pins are for use with an external speaker. If you wish to use the onboard speaker, you should close pins 6-7 with a jumper.

Speaker Connector Pin Definitions (JD1)

Pin Number	Function	Definition
4	+	Red wire, Speaker data
5	Key	No connection
6	Key	Key
7	Speaker data	Speaker data

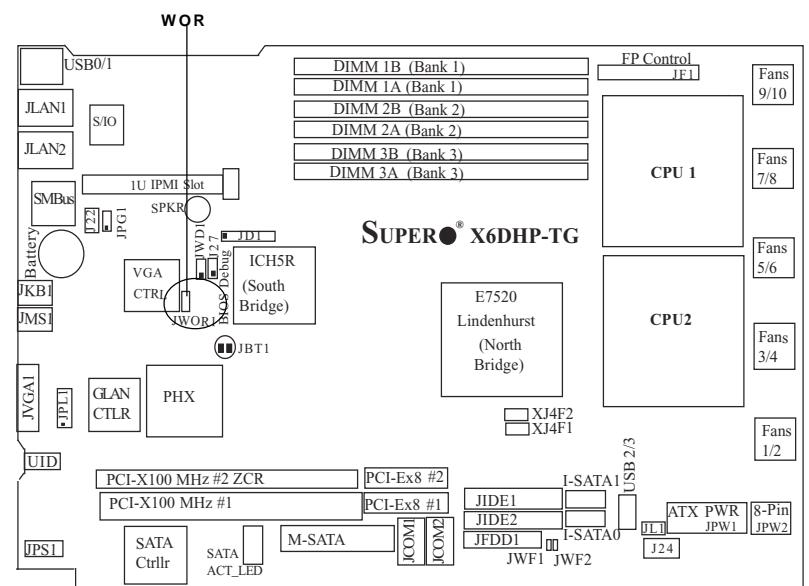


Wake-On-Ring

The Wake-On-Ring header is designated JWOR1. This function allows your computer to receive and "wake-up" by an incoming call to the modem when in suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and cable to use this feature.

Wake-on-Ring Pin Definitions (JWOR1)

Pin Number	Definition
1	Ground
2	Wake-up



SMB

A System Management Bus header is located at J22. Connect the appropriate cable here to utilize SMB on your system.

Fan Header Pin Definitions

Pin Number	Definition
1	Ground (black)
2	+12V (red)
3	Tachometer

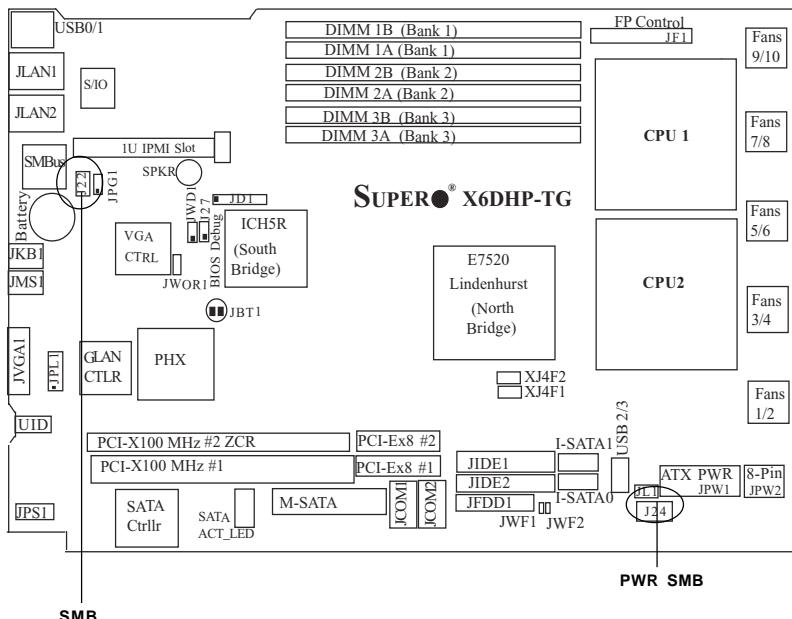
Caution: These fan headers are DC power.

Power SMB (I² C) Connector

I² C Connector (J24), located between the PWR ForceOn Header and the PWR Fault header, monitors the status of PWR Supply, Fan and system temperature.

SMB PWR Pin Definitions (J24)

Pin #	Definition
1	Clock
2	Data
3	PW Fail Signal
4	N/A
5	N/A

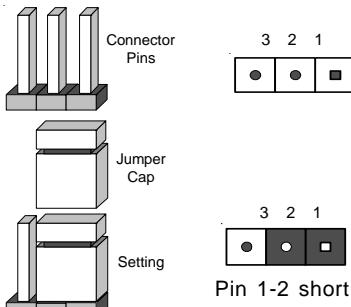


2-6 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

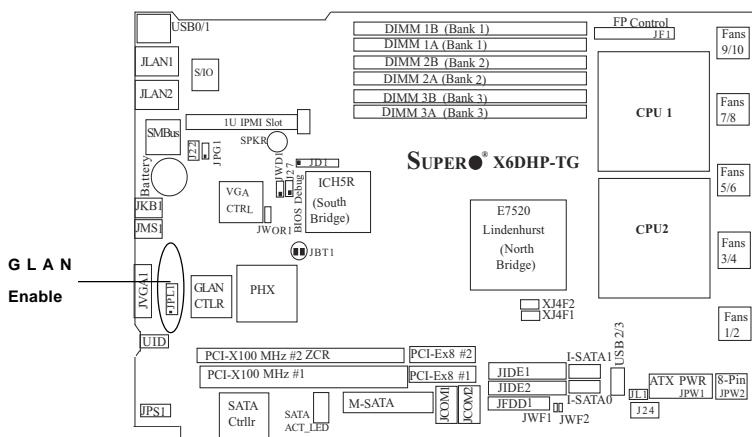


GLAN Enable/Disable

JPL1 enables or disables the GLAN ports on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

GLAN Enable/Disable Jumper Settings (JPL1)

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



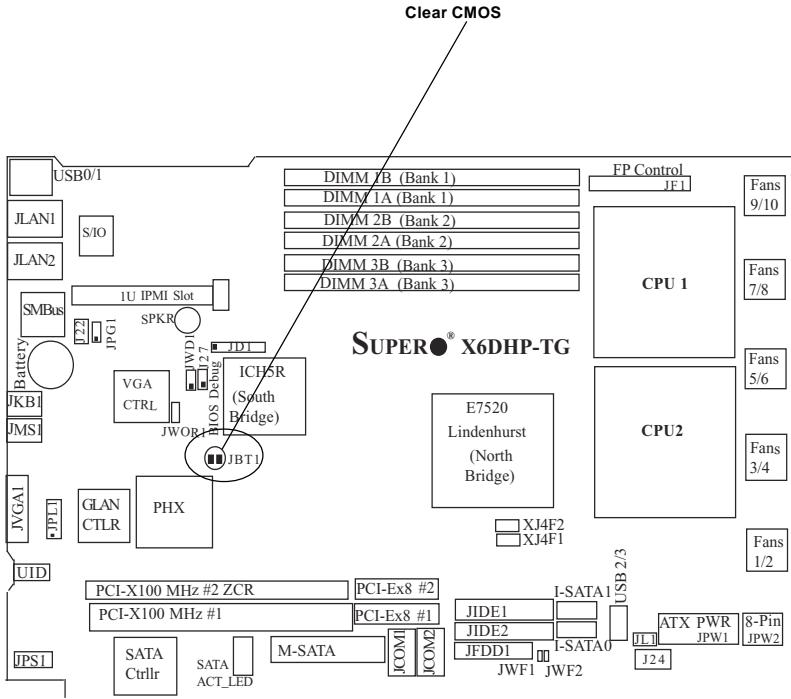
CMOS Clear

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent the accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection. Always remove the AC power cord from the system before clearing CMOS.



Note: For an ATX power supply, you must completely shut down the system, remove the AC power cord and then short JBT1 to clear CMOS.

Do not use the PW ON connector to clear CMOS.



VGA Enable/Disable

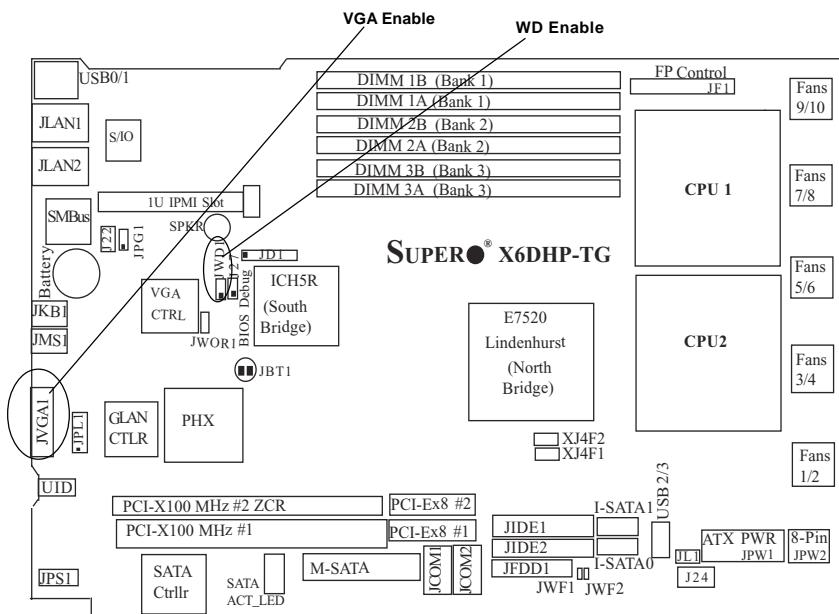
JPG1 enables or disables the VGA Connector on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

VGA Enable/Disable Jumper Settings (JPG1)	
Jumper Position	Definition
Jins 1-2	Enabled
Jins 2-3	Disabled

Watch Dog Enable

JWD1 controls Watch Dog, a system monitor that takes action when a software application freezes the system. Pins 1-2 will have WD reset the system if a program freezes. Pins 2-3 will generate a non-maskable interrupt for the program that has frozen (requires software implementation). Watch Dog must also be enabled in BIOS.

Watch Dog Jumper Settings (JWD1)	
Jumper Position	Definition
Pins 1-2	WD to Reset
Pins 2-3	WD to NMI
Open	Disabled



JP17 JP18

BIOS Debug (J27)

Close pins 2 and 3 of J27 to allow the system to search for the rescue disk from the floppy drive to refresh the BIOS when the BIOS crashes. Refer to the Table on the right for pin definitions.

BIOS Debug
Pin Definitions (J27)

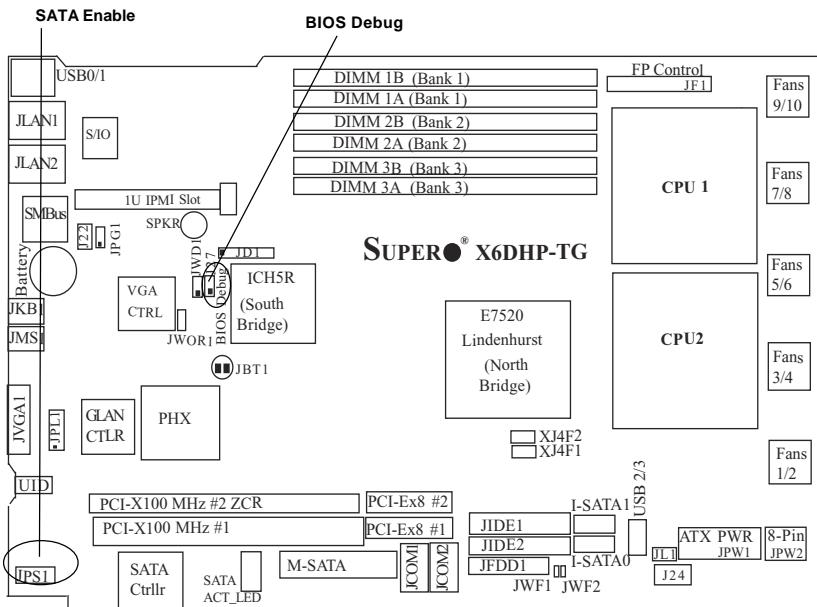
Pin Number	Definition
1-2	Normal
2-3	BIOS Recovery

SATA Enable

Close pins 1 and 2 of JPS1 to enable SATA controller. Refer to the Table on the right for pin definitions.

SATA Enable/Disable
Jumper Settings
(JPS1)

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



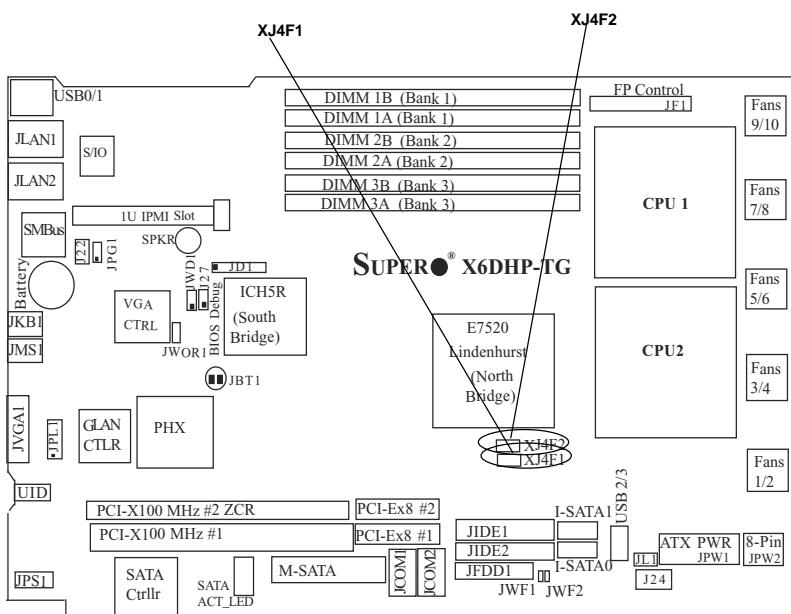
PLLSEL Select

XJ4F1/XJ4F2 allows the user to select PLLSEL (memory speed). See the table on the right for jumper definitions. (*The Default setting is: Closed: DDR333).

PLLSEL Select Jumper Settings (XJ4F1/XJ4F2)

DDR	XJ4F1	XJ4F2
333 MHz	Closed	Closed
266MHz	Open	Open

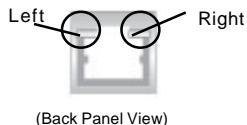
(*Default: DDR333)



2-7 Onboard Indicators

GLAN LEDs

Each Gigabit Ethernet LAN port (located between SMBus and USB0/1 connectors) has two LEDs on the back of the connectors. The yellow LED indicates activity while the other LED may be green, orange or off to indicate the speed of the connection. See the table at right for the functions associated with the second LED.



(Back Panel View)

**1 Gb LAN Left LED
Indicator (Speed LED)**

LED Color	Definition
Off	10 Mbps or *NC
Green	100 Mbps
Amber	1 Gbps

*NC=No Connection

**1 Gb LAN Right LED
Indicator (Activity LED)**

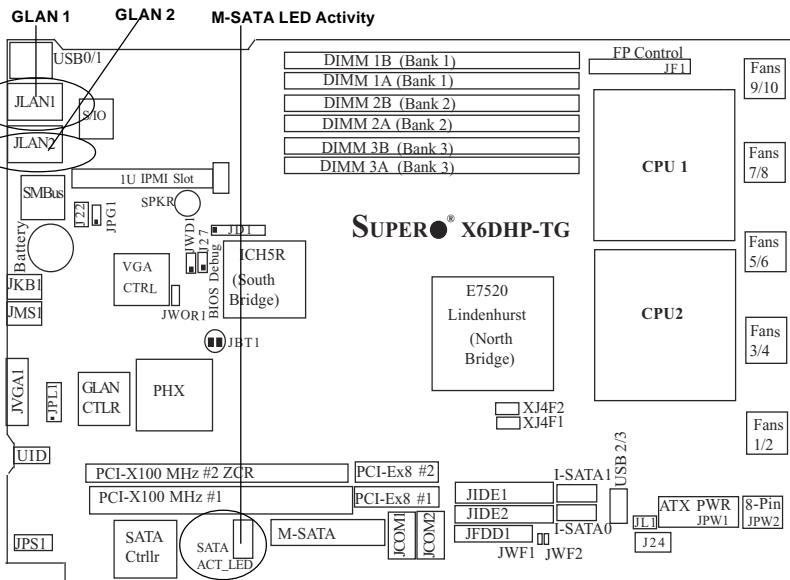
LED Color	Definition
Amber	Blinking 10/100Mbps/1Gbps

Marvell SATA Activity LED Indicator

Marvell Serial ATA Activity LED Indicator, located between SATA Controller and Marvell SATA 4-port connector, displays the status of Marvell's SATA Activities. See the table on the right for pin definitions.

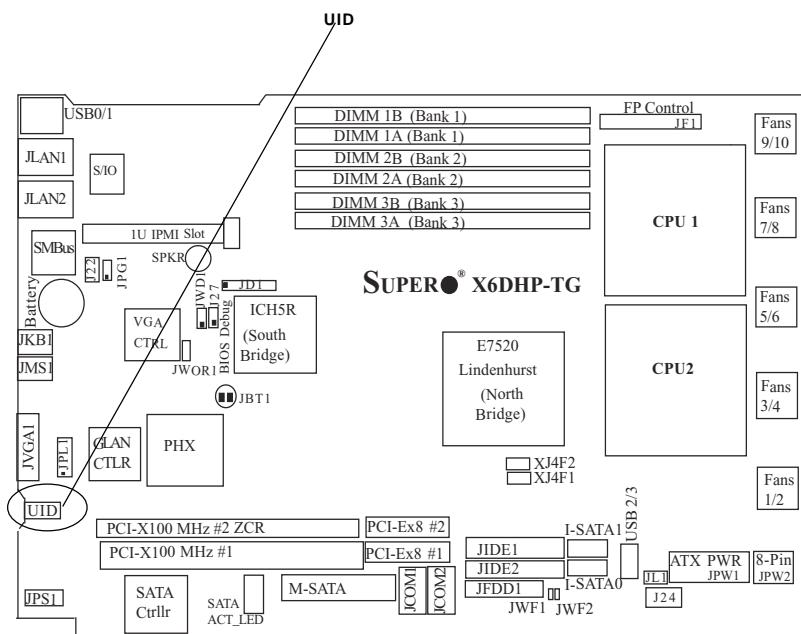
M-SATA_ACT_Indicator Pin Definitions

Pin #	Definition	Pin #	Definition
1	SATA0_Act	6	NC
2	SATA1_Act	7	NC
3	SATA2_Act	8	NC
4	SATA3_Act	9	NC
5	LED_COM	10	NC



Unit Identification

A Unit Identification Indicator (UID) is located above SCSI Channel B Connector. Use this indicator for easy identification of the system when needed. While servicing the system, if you find you need to work on the other side of the unit, push the UID button (located on the far left of the control panel on the front and to the right of the VGA port on the rear of the chassis) to illuminate an LED on the other side of the chassis. When you walk around to the other side of the rack, the unit will then be easy to spot. The LED will remain on until the UID button is pushed again.



2-8 Floppy Drive, Hard Disk Drive and 1U IPMI Connections

Note the following when connecting the floppy and hard disk drive cables:

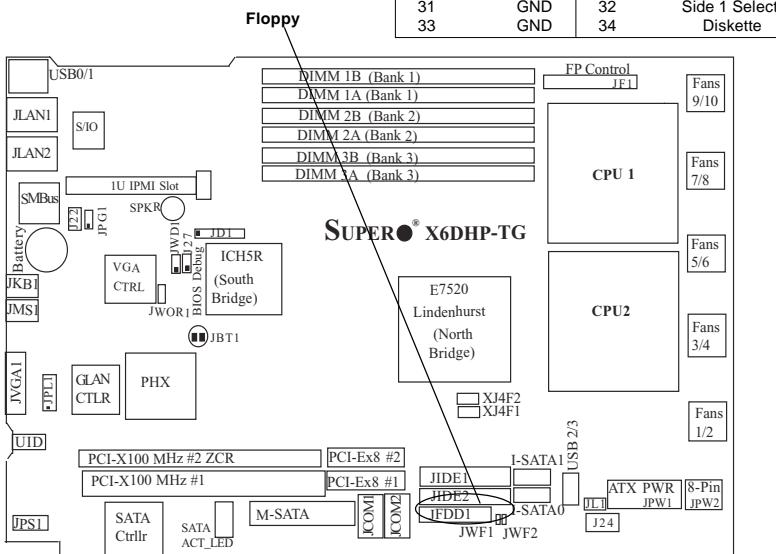
- The floppy disk drive cable has seven twisted wires.
- A red mark on a wire typically designates the location of pin 1.
- A single floppy disk drive ribbon cable has 34 wires and two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.

Floppy Connector

The floppy connector is located on JFDD1. See the table below for pin definitions.

Floppy Connector Pin Definitions

Pin Number	Function	Pin Number	Function
1	GND	2	FDHDIN
3	GND	4	Reserved
5	Key	6	FDDEDIN
7	GND	8	Index-
9	GND	10	Motor Enable
11	GND	12	Drive Select B-
13	GND	14	Drive Select A-
15	GND	16	Motor Enable
17	GND	18	DIR-
19	GND	20	STEP-
21	GND	22	Write Data-
23	GND	24	Write Gate-
25	GND	26	Track 00-
27	GND	28	Write Protect-
29	GND	30	Read Data-
31	GND	32	Side 1 Select-
33	GND	34	Diskette



IDE Connectors

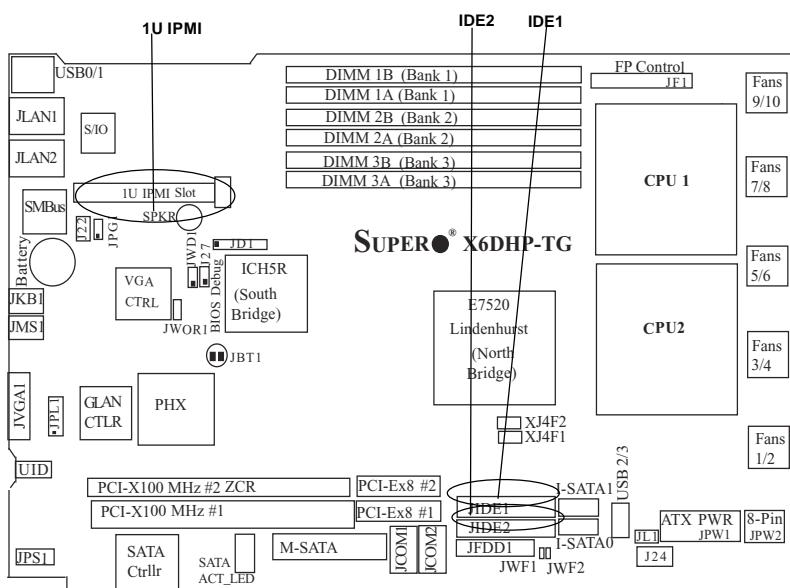
There are no jumpers to configure the onboard IDE#1 and #2 connectors (at JIDE1 and JJIDE2, respectively). See the table on the right for pin definitions.

IDE Connector Pin Definitions
(J5, J6)

Pin Number	Function	Pin Number	Function
1	Reset IDE	2	GND
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	GND	20	Key
21	DRQ3	22	GND
23	I/O Write-	24	GND
25	I/O Read-	26	GND
27	IOCHRDY	28	BALE
29	DACK3-	30	GND
31	IRQ14	32	IOCS16-
33	Addr 1	34	GND
35	Addr 0	36	Addr 2
37	Chip Select 0	38	Chip Select 1-
39	Activity	40	GND

1U IPMI Slot

J1 is designated as the 1U IPMI Slot on the Motherboard.



Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter.

Note: Always disconnect the power cord before adding, changing or installing any hardware components.

Before Power On

1. Make sure that there is not any short circuit between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Properly install the CPU and make sure that the CPU is fully seated in the socket. Connect the chassis speaker and the power LED to the motherboard. (Check all jumper settings as well.)
5. Use only the correct type of CMOS onboard battery as recommended by the Manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

No Power

1. Make sure that there is not any short circuit between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to the Appendix for details on beep codes.

NOTE

If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to App. B.

Memory Errors

1. Make sure the DIMM modules are properly and fully installed.
2. Determine if different speeds of DIMMs have been installed and verify that the BIOS setup is configured for the fastest speed of RAM used. It is recommended to use the same RAM speed for all DIMMs in the system.
3. Make sure you are using the correct type of Registered ECC DDR 266/333 (PC 2100/PC2700) SDRAM (*Please refer to Page 2-5 for the instruction on DDR 266/333 DIMM population and installation.)
4. Check for bad DIMM modules or slots by swapping a single module between two slots and noting the results.
5. Make sure all memory modules are fully seated in their slots. As an interleaved memory scheme is used, you must install two modules at a time, beginning with Bank 1, then Bank 2, and so on (see Section 2-3).
6. Check the position of the 115V/230V switch on the power supply.

Losing the System's Setup Configuration

1. Ensure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, Super Micro does not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our web site (<http://www.supermicro.com/support/faqs/>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our web site at (<http://www.supermicro.com/support/bios/>).

Note: Not all BIOS can be flashed depending on the modifications to the boot block code.

3. If you still cannot resolve the problem, include the following information when contacting Super Micro for technical support:

- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration

An example of a Technical Support form is on our web site at (<http://www.supermicro.com/support/contact.cfm>).

4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.

3-3 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The X6DHP-TG has eight 184-pin DIMM slots that support registered ECC DDR 333/266 SDRAM modules. It is strongly recommended that you do not mix memory modules of different speeds and sizes. (*In order to support 6 DIMMs, single rank memory modules must be used.)

Question: How do I update my BIOS?

Answer: It is recommended that you do not upgrade your BIOS if you are experiencing no problems with your system. Updated BIOS files are located on our web site at <http://www.supermicro.com>. Please check our BIOS warning message and the info on how to update your BIOS on our web site. Also, check the current BIOS revision and make sure it is newer than your BIOS before downloading. Select your motherboard model and download the BIOS file to your computer. Unzip the BIOS update file and you will find the readme.txt (flash instructions), the phlash.exe (BIOS flash utility), the platform.bin (platform file) and the BIOS image (xxxxxx.rom) files. Copy

these files into a bootable floppy and reboot your system. Then, follow Readme.txt to continue flashing the BIOS.

Question: What's on the CD that came with my motherboard?

Answer: The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the applications you need. Applications on the CD include chipset drivers for Windows and security and audio drivers.

3-4 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS

4-1 Introduction

This chapter describes the Phoenix BIOS™ Setup utility for the X6DHP-TG. The Phoenix ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of the Supermicro web site <<http://www.supermicro.com>> for any changes to BIOS that may not be reflected in this manual.

System BIOS

The BIOS is the Basic Input Output System used in all IBM® PC, XT™, AT®, and PS/2® compatible computers. The Phoenix BIOS flash chip stores the system parameters, such type of disk drives, video displays, etc. in the CMOS. The CMOS memory requires very little electrical power. When the computer is turned off, a backup battery provides power to the BIOS flash chip, enabling it to retain system parameters. Each time the computer is powered-on the computer is configured with the values stored in the BIOS ROM by the system BIOS, which gains control at boot-up.

How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing the <Delete> key at the appropriate time during system boot, see below.

Starting the Setup Utility

Normally, the only visible POST (Power On Self Test) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens, such as the Security and Power menus. Beginning with Section 4-3, detailed descriptions are given for each parameter setting in the Setup utility.

4-2 Running Setup

**Default settings are in bold text unless otherwise noted.*

The BIOS setup options described in this section are selected by choosing the appropriate text from the main BIOS Setup screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options (see on next page).

When you first power on the computer, the Phoenix BIOS™ is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <Delete> immediately after turning the system on, or
2. When the message shown below appears briefly at the bottom of the screen during the POST (Power On Self-Test), press the <Delete> key to activate the main Setup menu:

Press the <Delete> key to enter Setup

(*Note: Please load "System Setup Default" when using the system the first time.)

4-3 Main BIOS Setup

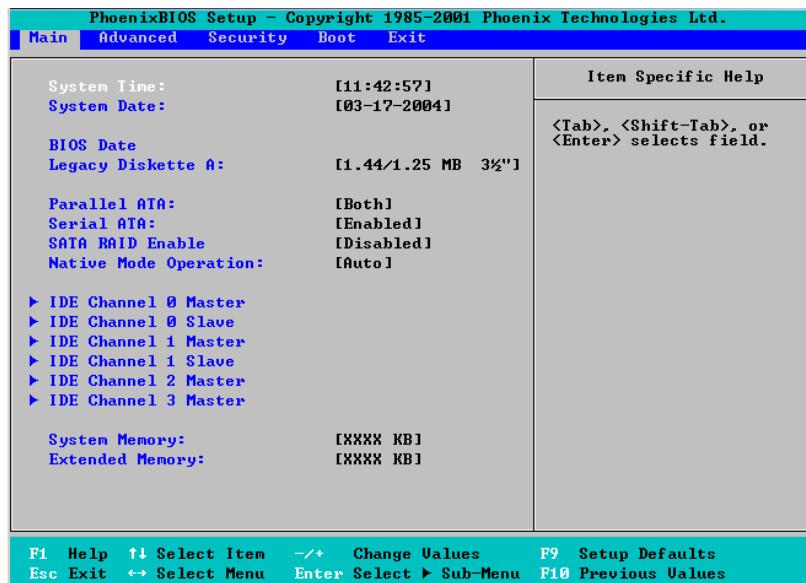
All main Setup options are described in this section. The main BIOS Setup screen is displayed below.

Use the Up/Down arrow keys to move among the different settings in each menu. Use the Left/Right arrow keys to change the options for each setting.

Press the <Esc> key to exit the CMOS Setup Menu. The next section describes in detail how to navigate through the menus.

Items that use submenus are indicated with the ► icon. With the item highlighted, press the <Enter> key to access the submenu.

Main BIOS Setup Menu



Main Setup Features

System Time

To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.

System Date

Using the arrow keys, highlight the month, day and year fields and enter the correct data. Press the <Enter> key to save the data.

BIOS Date

This feature allows BIOS to automatically display the BIOS date.

Legacy Diskette A

This setting allows the user to set the type of floppy disk drive installed as diskette A. The options are Disabled, 360Kb 5.25 in, 1.2MB 5.25 in, 720Kb 3.5 in, **1.44/1.25MB**, **3.5** in and 2.88MB 3.5 in.

Parallel ATA

This setting allows the user to enable or disable the function of Parallel ATA. The options are Disabled, Channel 0, Channel 1 and **Both**.

Serial ATA

This setting allows the user to enable or disable the function of Serial ATA. The options are Disabled and **Enabled**.

Serial ATA RAID Enable

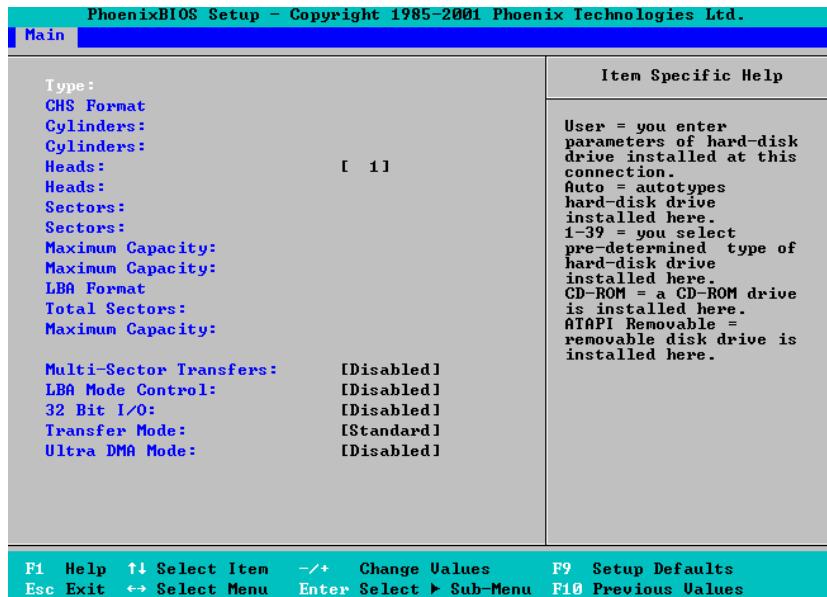
Select Enable to enable Serial ATA RAID Functions. (*For the OS environment, use the RAID driver if this feature is set to Enabled.) The default setting is **Disabled**.

Native Mode Operation

Select the native mode for ATA. The options are: Parallel ATA, Serial ATA, Both, and **Auto**.

►IDE Channel 0 Master/Slave, IDE Channel 1 Master/Slave, IDE Channel 2 Master, IDE Channel 3 Master

These settings allow the user to set the parameters of IDE Channel 0 Master/Slave, IDE Channel 1 Master/Slave, IDE Channel 2 Master, IDE Channel 3 Master slots. Hit <Enter> to activate the following sub-menu screen for detailed options of these items. Set the correct configurations accordingly. The items included in the sub-menu are:



Type

Selects the type of IDE hard drive. The options are **Auto**, (which allows BIOS to automatically determine the hard drive's capacity, number of heads, etc.), a number from 1-39 to select a predetermined type of hard drive, CDROM and ATAPI Removable. The option- "User" will allow the user to enter the parameters of the HDD installed at this connection. The option-"Auto" will allow BIOS to automatically configure the parameters of the HDD installed at the connection. Choose the option 1-39 to select a predetermined HDD type. Select CDROM if a CDROM drive is installed. Select ATAPI if a removable disk drive is installed.

Multi-Sector Transfer

This item allows the user to specify the number of sectors per block to be used in multi-sector transfer. The options are **Disabled**, **4 Sectors**, **8 Sectors** and **16 Sectors**.

LBA Mode Control

This item determines whether Phoenix BIOS will access the IDE Channel 0 Master Device via the LBA mode. The options are **Enabled** and **Disabled**.

32 Bit I/O

This option allows the user to enable or disable the function of 32-bit data transfer. The options are **Enabled** and **Disabled**.

Transfer Mode

Selects the transfer mode. The options are **Standard**, Fast PIO1, Fast PIO2, Fast PIO3, Fast PIO4, FPIO3/DMA1 and FPIO4/DMA2.

Ultra DMA Mode

Selects Ultra DMA Mode. The options are **Disabled**, **Mode 0**, **Mode 1**, **Mode 2**, **Mode 3**, **Mode 4** and **Mode 5**.

System Memory

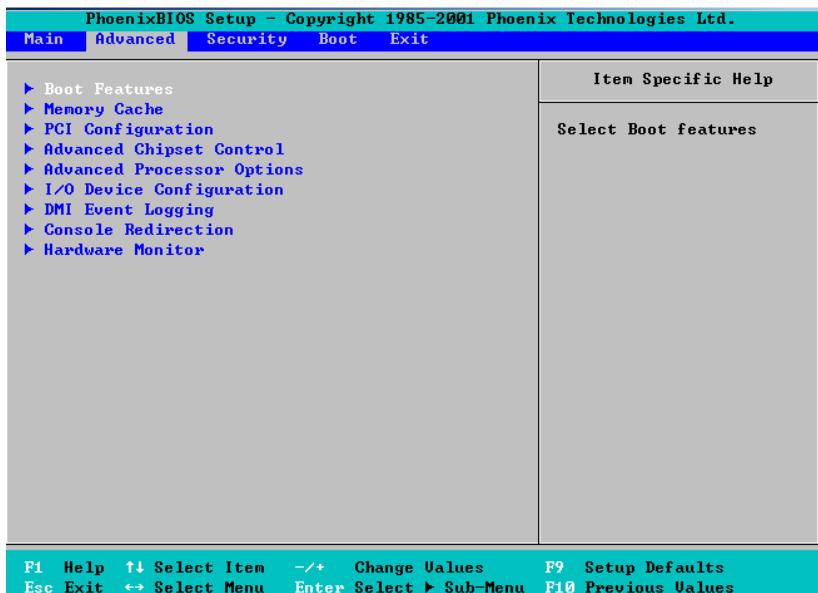
This display informs you how much system memory is recognized as being present in the system.

Extended Memory

This display informs you how much extended memory is recognized as being present in the system.

4-4 Advanced Setup

Choose Advanced from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. The items with a triangle beside them have sub menus that can be accessed by highlighting the item and pressing <Enter>. Options for PIR settings are displayed by highlighting the setting option using the arrow keys and pressing <Enter>. All Advanced BIOS Setup options are described in this section.



►Boot Features

Access the submenu to make changes to the following settings.

Quick Boot Mode

If enabled, this feature will speed up the POST (Power On Self Test) routine by skipping certain tests after the computer is turned on. The settings are **Enabled** and **Disabled**. If **Disabled**, the POST routine will run at normal speed.

Quiet Boot

This setting allows you to **Enable** or **Disable** the diagnostic screen during boot-up.

ACPI Mode

Use the setting to determine if you want to employ ACPI (Advanced Configuration and Power Interface) power management on your system. The options are **Yes** and **No**.

Power Button Behavior

If set to Instant-Off, the system will power off immediately as soon as the user hits the power button. If set to 4-sec., the system will power off when the user presses the power button for 4 seconds or longer. The options are **instant-off** and 4-sec override.

After Power Failure

This setting allows you to choose how the system will react when power returns after an unexpected loss of power. The options are **Stay Off** and Power On.

Watch Dog

If enabled, this option will automatically reset the system if the system is not active for more than 5 minutes. The options are **Enabled** and **Disabled**.

Summary Screen

This setting allows you to **Enable** or **Disable** the summary screen which displays the system configuration during bootup. **After Power Failure**

This setting allows you to choose how the system will react when power returns after an unexpected loss of power. The options are **Stay Off** and Power On.

►Memory Cache

Cache System BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a System BIOS buffer to allow the BIOS write (cache) its data into this reserved memory area. Select "**Write Protect**" to enable this function, and this area will be reserved for BIOS ROM access only. Select "Uncached" to disable this function and make this area available for other devices.

Cache Video BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a Video BIOS buffer to allow the BIOS write (cache) its data into this reserved memory area. Select "**Write Protect**" to enable the function and this area will be reserved for Video BIOS ROM access only. Select "Uncached" to disable this function and make this area available for other devices

Cache Base 0-512K

If enabled, this feature will allow the data stored in the base memory area: block 0-512K to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or to be written into L1, L2 cache inside the CPU to speed up CPU operations . Select "Uncached" to disable this function. Select "Write Through" to allow data to be cached into the buffer and written into the system memory at the same time. Select "Write Protect" to prevent data from being written into the base memory area of Block 0-512K. Select "Write Back" to allow CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are "Uncached", "Write Through", "Write Protect", and "Write Back".

Cache Base 512K-640K

If enabled, this feature will allow the data stored in the memory area: 512K-640K to be cached (written) into a buffer, a storage area in the Static DRM (SDROM) or written into L1, L2, L3 cache inside the CPU to speed up CPU operations . Select "Uncached" to disable this function. Select "Write Through" to allow data to be cached into the buffer and written into the system memory at the same time. Select "Write Protect" to prevent data from being written into the base memory area of Block 0-512K. Select "Write Back" to allow CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are "Uncached", "Write Through", "Write Protect", and "Write Back".

Cache Extended Memory

If enabled, this feature will allow the data stored in the extended memory area to be cached (written) into a buffer, a storage area in the Static DRM (SDROM) or written into L1, L2, L3 cache inside the CPU to speed up CPU operations . Select "Uncached" to disable this function. Select "Write Through" to allow data to be cached into the buffer and written into the system memory at the same time. Select "Write Protect" to prevent data from being written into the base memory area of Block 0-512K. Select "Write Back" to allow CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are "Uncached", "Write Through", "Write Protect", and "Write Back".

Discrete MTRR Allocation

If enabled, MTRRs (-Memory Type Range Registers) are configured as distinct, separate units and cannot be overlapped. Select enabled to enhance graphic performance when you use a Linux graphics driver that requires write-combining with 4GB or more memory. The options are **Enabled** and **Disabled**.

►PCI Configuration

Access the submenu to make changes to the following settings for PCI devices.

Onboard GLAN (Gigabit- LAN) OROM Configure

Enabling this option provides the capability to boot from GLAN. The options are **Disabled** and **Enabled**.

.Reset Configuration Data

If set to Yes, this setting clears the Extended System Configuration Data-(ESCD) area. The options are **Yes** and **No**.

Frequency for PCIX#1/GLAN

This option allows the user to change the bus frequency for the devices installed in the slot indicated. The options are **Auto**, PCI 33 MHz, PCI 66 MHz, PCI-X 66 MHz, PCI-X 100 MHz, and PCI-X 133 MHz.

Frequency for PCIX#2/MASS

This option allows the user to change the bus frequency of the devices installed in the slot indicated. The options are **Auto**, PCI 33 MHz, PCI 66 MHz, PCI-X 66 MHz, PCI-X 100 MHz, and PCI-X 133 MHz.

►PCI-X 100MHz Slot#1, PCI-X 133MHz Slot#2 w/ZCR, PCI-Express x8 Slot#1, PCI-Express x8 Slot#2

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and **Disabled**.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are **Enabled** and **Disabled**.

Latency Timer

This setting allows you to set the clock rate for Bus Master. A high-priority, high-throughput device may benefit from a greater Clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novell and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. Different OS requires different Bus Master clock rate.

Large Disk Access Mode

This setting determines how large hard drives are to be accessed. The options are **DOS** or **Other** (for Unix, Novell NetWare and other operating systems).

►Advanced Chipset Control

Access the submenu to make changes to the following settings.

Force Compliance Mode

This feature allows you to enable the PCI-Express Compliance Mode. The options are: **Disabled** or **Enabled**.

Memory RAS Feature Control

Select this option in order to enable the special feature of DIMM sparing or memory mirroring. The options are **Mirroring**, **Sparing** and **Standard**.

Clock Spectrum Feature

If "Enabled", BIOS will monitor the level of Electromagnetic Interference caused by the components and will attempt to decrease the interference whenever needed. The options are **Enabled** and **Disabled**.

DRAM Data Integrity Mode

If enabled, this feature allows the data stored in the DRMA memory to be integrated for faster data processing. The options are **72-bit ECC**, **144-bit ECC**, **Auto** and **Disabled**.

ECC Error Type

This setting lets you select which type of interrupt to be activated as a result of an ECC error. The options are None, NMI (Non-Maskable Interrupt), SMI (System Management Interrupt) and SCI (System Control Interrupt.)

SERR Signal Condition

This setting specifies the ECC Error conditions that an SERR# is to be asserted. The options are None, **Single Bit**, Multiple Bit and Both.

USB Function

This setting allows you to **Enable** or Disable all functions for the Onboard USB devices. The options are **Enabled** and **Disabled**.

Legacy USB Support

This setting allows you to enable support for Legacy USB devices. The settings are **Enabled** and **Disabled**.

►Advanced Processor Options

Access the submenu to make changes to the following settings.

CPU Speed

This is a display that indicates the speed of the installed processor.

Hyper-threading

This setting allows you to **Enable** or **Disable** the function of Hyper-Threading. Enabling Hyper Threading results in increased CPU performance. (Applicable for the XP systems.)

Machine Checking

Enable to allow the operating system to debug a system crash after a reset. The options are **Disabled** and **Enabled**.

Adjacent Cache Line Prefetch

The CPU fetches the cache line for 64 bytes if **Disabled**. The CPU fetches both cache lines for 128 bytes as comprised if **Enabled**.

Processor Power Management

This feature allows you to decide how processor power is managed. If set to **Disabled**, processor power is not managed based on the power demand on the CPU, and demand-based power switching is not allowed. If set to **GV3**, processor power will be controlled by the Bus Ratio and CPU voltage. Demand-based switching is allowed. The options are **Disabled** and **GV3**.

►I/O Device Configuration

Access the submenu to make changes to the following settings.

KBC Clock input

This setting allows you to set the clock frequency for the Keyboard Clock. The options are 8MHz, **12 MHz** and 16MHz.

Onboard COM1

This setting allows you to assign control of Onboard COM1. The options are **Enabled** (user defined), Disabled, and Auto (BIOS- or OS- controlled).

Base I/O Address

Select the base I/O address for COM1. The options are **3F8/IRQ4**, 2F8/IRQ3, 3E8/IRQ4 and 2E8/IRQ3.

Onboard COM2

This setting allows you to assign control of Onboard COM2. The options are **Enabled** (user defined), Disabled, and Auto (BIOS- or OS- controlled).

Base I/O Address

Select the base I/O address for COM2. The options are **3F8/IRQ4**, 2F8/IRQ3, 3E8/IRQ4 and 2E8/IRQ3.

Floppy Disk Controller

This setting allows you to assign control of the floppy disk controller. The options are **Enabled** (user defined), Disabled, and Auto (BIOS and OS controlled).

►DMI Event Logging

Access the submenu to make changes to the following settings.

Event Log Validity

This is a display to inform you of the event log validity. It is not a setting.

Event Log Capacity

This is a display to inform you of the event log capacity. It is not a setting.

View DMI Event Log

Highlight this item and press <Enter> to view the contents of the event log.

Event Logging

This setting allows you to **Enable** or Disable event logging.

ECC Event Logging

This setting allows you to **Enable** or Disable ECC event logging.

Mark DMI Events as Read

Highlight this item and press <Enter> to mark the DMI events as read.

Clear All DMI Event Logs

Select Yes and press <Enter> to clear all DMI event logs. The options are Yes and **No**.

►Console Redirection

Access the submenu to make changes to the following settings.

COM Port Address

This item allows you to specifies to redirect the console to Onboard COM A or Onboard COM B. This setting can also be **Disabled**.

BAUD Rate

This item allows you to select the BAUD rate for console redirection. The options are 300, 1200, 2400, 9600, **19.2K**, 38.4K, 57.6K and 115.2K.

Console Type

This item allows you to choose from the available options to select the console type for console redirection. The options are VT100, VT100,8bit, PC-ANSI, 7bit, **PC ANSI**, VT100+, VT-UTF8.

Flow Control

This item allows you to choose from the available options to select the flow control for console redirection. The options are: None, XON/XOFF, and **CTS/RTS**.

Console Connection

This item allows you to choose select the console connection: either **Direct** or **Via Modem**.

Continue CR after POST

Choose whether to continue with console redirection after the POST routine. The options are **On** and **Off**.

►Hardware Monitor Logic

CPU Temperature Threshold

This option allows the user to set a CPU temperature threshold that will activate the alarm system when the CPU temperature reaches this pre-set temperature threshold. The options are 75°C, **80°C**, 85°C and 90°C.

Highlight this and hit <Enter> to see the data for the following items:

Temperature: These items display the Temperature status of the following items.

CPU1 Temperature

CPU2 Temperature

LM93 Temperature

LM30 Temperature

Fan Speed Control Modes [Disable, 3-pin (Server)]

This feature allows the user to decide how the system controls the speeds of onboard fans. Select "Disable" to disable of the function of fan speed control and the system will run at the full speed (12V.) Select "3-pin Server" to optimize the fan speed control via BIOS Thermal management.

Fan 1 - FAN 10: If the feature of Auto Fan Control is enabled, BIOS will automatically display the status of the fans indicated in this item.

Voltage: These items display the Voltage status of the following items.

P12V_CPU1-_Scaled

P12V_CPU2_Scaled

P12V_Scaled

CPU vio

MCH/PXH Vcore

ICH Vcore

CPU1 Vcore

CPU2 Vcore

P3V3

P5V

P1V8

Vdimm

HECETA_MEM_VTT

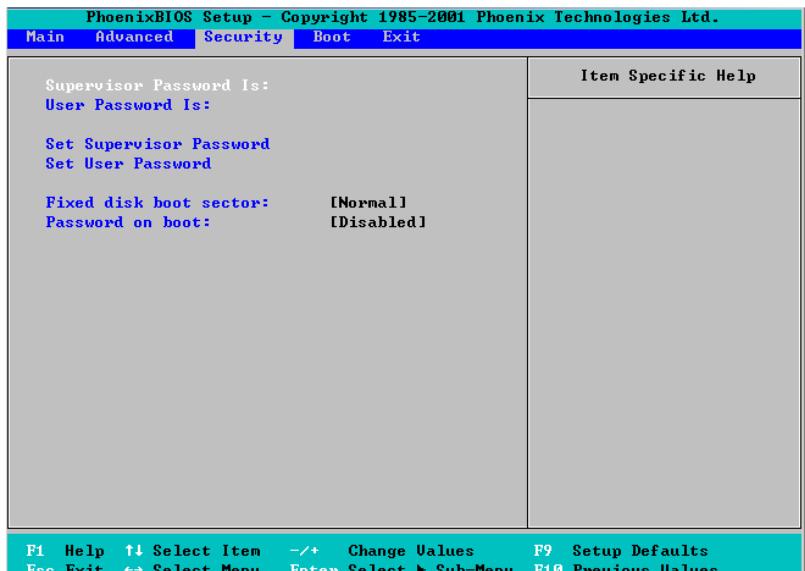
P1V0_NIC

N12V_Scaled

P3V3_STBY

4-5 Security

Choose Security from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <Enter>. All Security BIOS settings are described in this section.



Supervisor Password Is:

This displays whether a supervisor password has been entered for the system. Clear means such a password has not been used and Set means a supervisor password has been entered for the system.

User Password Is:

This displays whether a user password has been entered for the system. Clear means such a password has not been used and Set means a user password has been entered for the system.

Set Supervisor Password

When the item "Set Supervisor Password" is highlighted, hit the <Enter> key. When prompted, type the Supervisor's password in the dialogue box to set or to change supervisor's password, which allows access to BIOS.

Set User Password

When the item "Set User Password" is highlighted, hit the <Enter> key. When prompted, type the user's password in the dialogue box to set or to change the user's password, which allows access to the system at boot-up.

Fixed Disk Boot Sector

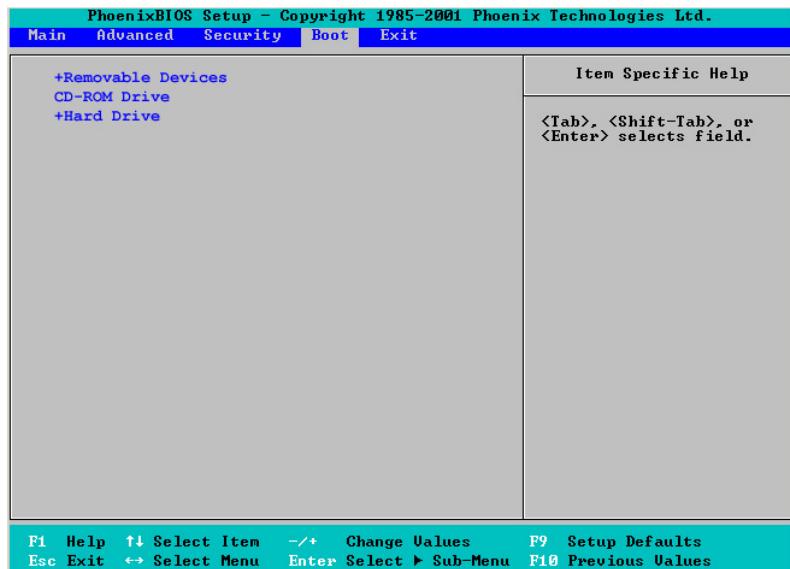
This setting may offer some protection against viruses when set to Write Protect, which protects the boot sector on the hard drive from having a virus written to it. The other option is **Normal**.

Password on Boot

This setting allows you to require a password to be entered when the system boots up. The options are Enabled (password required) and Disabled (password not required).

4-6 Boot

Choose Boot from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. Highlighting a setting with a + or - will expand or collapse that entry. See details on how to change the order and specs of boot devices in the Item Specific Help window. All Boot BIOS settings are described in this section.



+Removable Devices

Highlight and press <Enter> to expand the field. See details on how to change the order and specs of devices in the Item Specific Help window.

CDROM Drive

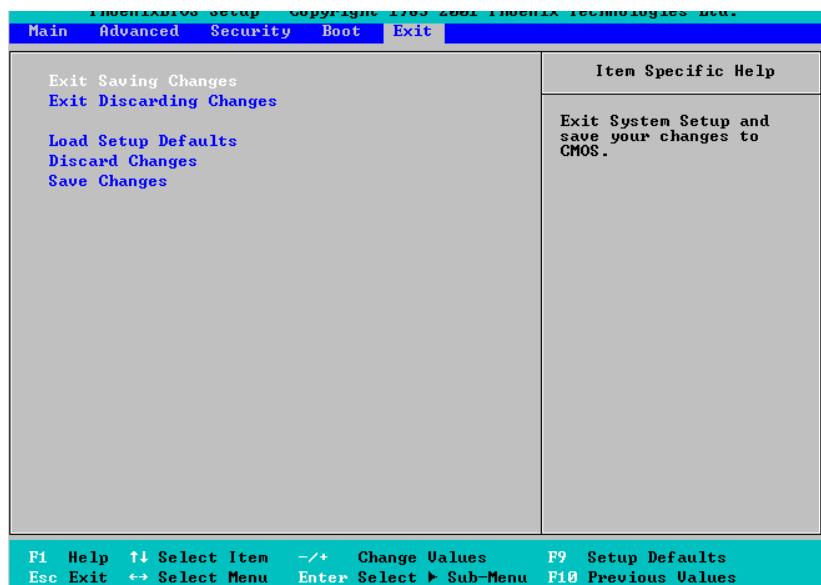
See details on how to change the order and specs of the CDROM drive in the Item Specific Help window.

+Hard Drive

Highlight and press <Enter> to expand the field. See details on how to change the order and specs of hard drives in the Item Specific Help window.

4-7 Exit

Choose Exit from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. All Exit BIOS settings are described in this section.



Exit Saving Changes

Highlight this item and hit <Enter> to save any changes you made and to exit the BIOS Setup utility.

Exit Discarding Changes

Highlight this item and hit <Enter> to exit the BIOS Setup utility without saving any changes you may have made.

Load Setup Defaults

Highlight this item and hit <Enter> to load the default settings for all items in the BIOS Setup. These are the safest settings to use.

Discard Changes

Highlight this item and hit <Enter> to discard (cancel) any changes you made. You will remain in the Setup utility.

Save Changes

Highlight this item and hit <Enter> to save any changes you made. You will remain in the Setup utility.

Notes

Appendix A

BIOS POST Codes

This section lists the POST (Power On Self Test) codes for the Phoenix BIOS. POST codes are divided into two categories: recoverable and terminal.

Recoverable POST Errors

When a recoverable type of error occurs during POST, the BIOS will display an POST code that describes the problem. BIOS may also issue one of the following beep codes:

- 1 long and two short beeps - video configuration error
- 1 repetitive long beep - no memory detected

Terminal POST Errors

If a terminal type of error occurs, BIOS will shut down the system. Before doing so, BIOS will write the error to port 80h, attempt to initialize video and write the error in the top left corner of the screen.

The following is a list of codes that may be written to port 80h.

POST Code	Description
02h	Verify Real Mode
03h	Disable Non-Maskable Interrupt (NMI)
04h	Get CPU type
06h	Initialize system hardware
07h	Disable shadow and execute code from the ROM.
08h	Initialize chipset with initial POST values
09h	Set IN POST flag
0Ah	Initialize CPU registers
0Bh	Enable CPU cache
0Ch	Initialize caches to initial POST values
0Eh	Initialize I/O component
0Fh	Initialize the local bus IDE
10h	Initialize Power Management
11h	Load alternate registers with initial POST values
12h	Restore CPU control word during warm boot
13h	Initialize PCI Bus Mastering devices
14h	Initialize keyboard controller
16h	1-2-2-3 BIOS ROM checksum
17h	Initialize cache before memory Auto size

POST Code	Description
18h	8254 timer initialization
1Ah	8237 DMA controller initialization
1Ch	Reset Programmable Interrupt Controller
20h	1-3-1-1 Test DRAM refresh
22h	1-3-1-3 Test 8742 Keyboard Controller
24h	Set ES segment register to 4 GB
28h	Auto size DRAM
29h	Initialize POST Memory Manager
2Ah	Clear 512 kB base RAM
2Ch	1-3-4-1 RAM failure on address line xxxx*
2Eh	1-3-4-3 RAM failure on data bits xxxx* of low byte of memory bus
2Fh	Enable cache before system BIOS shadow
32h	Test CPU bus-clock frequency
33h	Initialize Phoenix Dispatch Manager
36h	Warm start shut down
38h	Shadow system BIOS ROM
3Ah	Auto size cache
3Ch	Advanced configuration of chipset registers
3Dh	Load alternate registers with CMOS values
41h	Initialize extended memory for RomPilot
42h	Initialize interrupt vectors
45h	POST device initialization
46h	2-1-2-3 Check ROM copyright notice
47h	Initialize I2O support
48h	Check video configuration against CMOS
49h	Initialize PCI bus and devices
4Ah	Initialize all video adapters in system
4Bh	QuietBoot start (optional)
4Ch	Shadow video BIOS ROM
4Eh	Display BIOS copyright notice
4Fh	Initialize MultiBoot
50h	Display CPU type and speed
51h	Initialize EISA board
52h	Test keyboard
54h	Set key click if enabled
55h	Enable USB devices
58h	2-2-3-1 Test for unexpected interrupts
59h	Initialize POST display service
5Ah	Display prompt "Press F2 to enter SETUP"
5Bh	Disable CPU cache

POST Code	Description
5Ch	Test RAM between 512 and 640 kB
60h	Test extended memory
62h	Test extended memory address lines
64h	Jump to UserPatch1
66h	Configure advanced cache registers
67h	Initialize Multi Processor APIC
68h	Enable external and CPU caches
69h	Setup System Management Mode (SMM) area
6Ah	Display external L2 cache size
6Bh	Load custom defaults (optional)
6Ch	Display shadow-area message
6Eh	Display possible high address for UMB recovery
70h	Display error messages
72h	Check for configuration errors
76h	Check for keyboard errors
7Ch	Set up hardware interrupt vectors
7Dh	Initialize Intelligent System Monitoring
7Eh	Initialize coprocessor if present
80h	Disable onboard Super I/O ports and IRQs
81h	Late POST device initialization
82h	Detect and install external RS232 ports
83h	Configure non-MCD IDE controllers
84h	Detect and install external parallel ports
85h	Initialize PC-compatible PnP ISA devices
86h	Re-initialize onboard I/O ports.
87h	Configure Motherboard Configurable Devices (optional)
88h	Initialize BIOS Data Area
89h	Enable Non-Maskable Interrupts (NMIs)
8Ah	Initialize Extended BIOS Data Area
8Bh	Test and initialize PS/2 mouse
8Ch	Initialize floppy controller
8Fh	Determine number of ATA drives (optional)
90h	Initialize hard-disk controllers
91h	Initialize local-bus hard-disk controllers
92h	Jump to UserPatch2
93h	Build MPTABLE for multi-processor boards
95h	Install CD ROM for boot
96h	Clear huge ES segment register
97h	Fix up Multi Processor table
98h	1-2 Search for option ROMs. One long, two short beeps on checksum failure

POST Code	Description
99h	Check for SMART Drive (optional)
9Ah	Shadow option ROMs
9Ch	Set up Power Management
9Dh	Initialize security engine (optional)
9Eh	Enable hardware interrupts
9Fh	Determine number of ATA and SCSI drives
A0h	Set time of day
A2h	Check key lock
A4h	Initialize typematic rate
A8h	Erase F2 prompt
AAh	Scan for F2 key stroke
ACh	Enter SETUP
AEh	Clear Boot flag
B0h	Check for errors
B1h	Inform RomPilot about the end of POST.
B2h	POST done - prepare to boot operating system
B4h	1 One short beep before boot
B5h	Terminate QuietBoot (optional)
B6h	Check password (optional)
B7h	Initialize ACPI BIOS
B9h	Prepare Boot
BAh	Initialize SMBIOS
BBh	Initialize PnP Option ROMs
BCh	Clear parity checkers
BDh	Display MultiBoot menu
BEh	Clear screen (optional)
BFh	Check virus and backup reminders
C0h	Try to boot with INT 19
C1h	Initialize POST Error Manager (PEM)
C2h	Initialize error logging
C3h	Initialize error display function
C4h	Initialize system error handler
C5h	PnPd dual CMOS (optional)
C6h	Initialize note dock (optional)
C7h	Initialize note dock late
C8h	Force check (optional)
C9h	Extended checksum (optional)
CAh	Redirect Int 15h to enable remote keyboard
CBh	Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk
CCh	Redirect Int 10h to enable remote serial video

POST Code	Description
CDh	Re-map I/O and memory for PCMCIA
CEh	Initialize digitizer and display message
D2h	Unknown interrupt

The following are for boot block in Flash ROM

POST Code	Description
E0h	Initialize the chipset
E1h	Initialize the bridge
E2h	Initialize the CPU
E3h	Initialize system timer
E4h	Initialize system I/O
E5h	Check force recovery boot
E6h	Checksum BIOS ROM
E7h	Go to BIOS
E8h	Set Huge Segment
E9h	Initialize Multi Processor
EAh	Initialize OEM special code
EBh	Initialize PIC and DMA
ECh	Initialize Memory type
EDh	Initialize Memory size
EEh	Shadow Boot Block
EFh	System memory test
F0h	Initialize interrupt vectors
F1h	Initialize Run Time Clock
F2h	Initialize video
F3h	Initialize System Management Manager
F4h	Output one beep
F5h	Clear Huge Segment
F6h	Boot to Mini DOS
F7h	Boot to Full DOS

* If the BIOS detects error 2C, 2E, or 30 (base 512K RAM error), it displays an additional word-bitmap (*xxxx*) indicating the address line or bits that failed. For example, "2C 0002" means address line 1 (bit one set) has failed. "2E 1020" means data bits 12 and 5 (bits 12 and 5 set) have failed in the lower 16 bits. The BIOS also sends the bitmap to the port-80 LED display. It first displays the checkpoint code, followed by a delay, the high-order byte, another delay, and then the loworder byte of the error. It repeats this sequence continuously.

Notes

Appendix B

Software Installation

After all the hardware has been installed, you must first configure the Adaptec Embedded Serial ATA RAID Driver before you install the Windows operating system. The necessary drivers are all included on the Supermicro bootable CDs that came packaged with your motherboard. (For Adaptec's SCSI Host RAID Utility, please refer to the CDs that came with your motherboard.)

B-1 Adaptec Embedded SATA RAID Controller Driver

Serial ATA (SATA)

Serial ATA (SATA) is a physical storage interface. It uses a single cable with a minimum of four wires to create a point-to-point connection between devices. SATA is a serial link which supports transfer rates from 150 MBps. Because the serial cables used in SATA are thinner than the traditional cables used in Parallel ATA (PATA), SATA systems have better airflow and can be installed in smaller chassis than Parallel ATA. In addition, the cables used in PATA can only extend to 40cm long, while SATA cables can extend up to one meter. Overall, SATA provides better functionality than PATA.

Adaptec Embedded SATA with HostRAID Controller Driver

Adaptec's Embedded Serial ATA RAID with HostRAID controller adds RAID functionality to the Serial ATA I/O controller by supporting RAID 0 (Striping) or RAID 1 (Mirroring) to enhance the industry's pioneer PCI-to-e host controller products. RAID striping (RAID 0) can greatly improve hard disk I/O performance because of its capability in striping data across multiple drives. RAID mirroring (RAID 1) allows the data to be simultaneously written to two drives, so critical data is always available even if one hard disk fails.

Due to this built-in functionality, your Supermicro motherboard is specially designed to keep pace with the increasing performance demands of today's computer systems by improving disk I/O throughput and providing data accessibility regardless of a single disk failure. By incorporating Adaptec Embedded Serial ATA into the motherboard design, Supermicro offers the user the benefits of SATA RAID without the high costs associated with RAID hardware.

Note: For Adaptec's RAID Driver Installation Instructions, please refer to the Adaptec RAID Controller User's Guide: "Emb_SA_RAID_UG.pdf", which is located in the CD that came with this motherboard. You can also download a copy of Adaptec's User's Guide from our web site at www.supermicro.com.

Using the Adaptec RAID Configuration Utility (ARC)

The Adaptec RAID Configuration Utility is an embedded BIOS Utility, including:

*Array Configuration Utility: Use this utility when you want to create, configure and manage arrays.

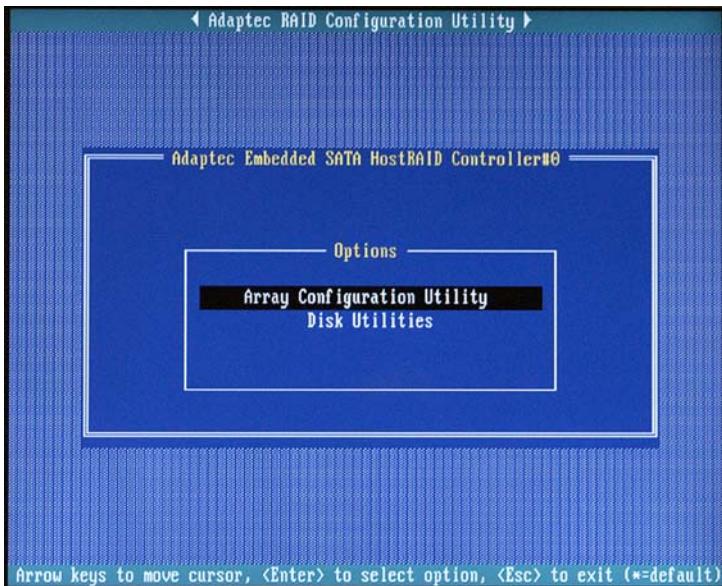
*Disk Utilities: Use this option to format or verify disks.

To run the Adaptec RAID Configuration Utility, you will need to enable the RAID function in the system BIOS (refer to Chapter 7 for System BIOS Configurations), and then press the **Ctrl** and **A** keys simultaneously when prompted to do so during the system startup. (Refer to the previous page for detailed instructions.)

Note: To select an option, use the arrow keys to highlight the item and then press the **Enter** key to select it. To return to the previous menu, press the **ESC** key.

Using the Array Configuration Utility (ACU)

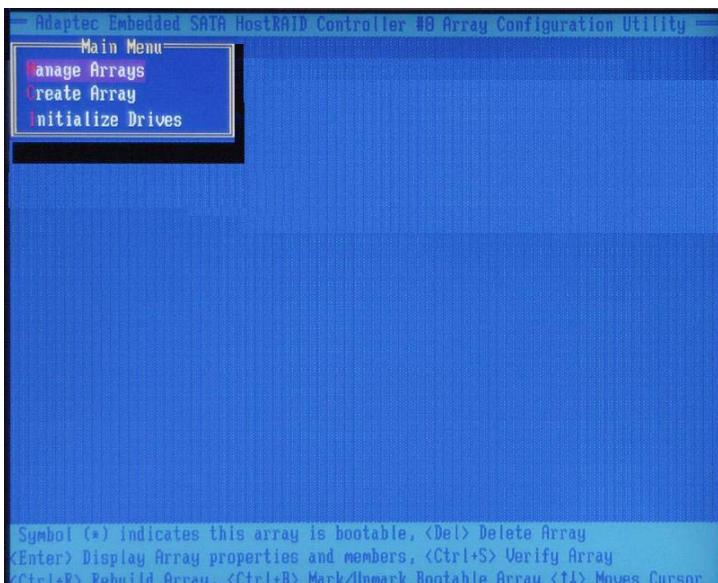
The Array Configuration Utility (ACU) enables you to create, manage, and delete arrays from the controller's BIOS, add and delete spare drives, and initialize drives. During the system startup, press the **Ctrl** and **A** keys simultaneously, and the main menu will appear.



Managing Arrays

Select this option to view array properties and delete arrays. The following sections describe the operations Of "Managing Arrays".

To select this option, use the arrow keys and the **Enter** key to select **Managing Arrays** from the main menu (as shown below).



Viewing Array Properties

To view the properties of an existing array:

1. At the BIOS prompt, press **Ctrl+A**.
2. From the ARC menu, select **Array Configuration Utility (ACU)**.
3. From the ACU menu, select **Manage Arrays** (as shown on the previous screen.)
4. From the List of Arrays dialog box, select the array you want to view and press **Enter**.

The Array Properties dialog box appears, showing detailed information on the array. The physical disks associated with the array are displayed here.

5. Press **Esc** to return to the previous menu.

Deleting Arrays

Warning: *Back up the data on an array before you delete it to prevent the loss of data. Deleted arrays cannot be restored.*

1. Turn on your computer and press **Ctrl+A** when prompted to access the ARC utility.
2. From the ARC main menu, select **Array Configuration Utility (ACU)**.
3. From the ACU menu, select **Manage Arrays**.
4. Select the array you wish to delete and press **Delete**.
5. In the Array Properties dialog box, select **Delete** and press **Enter**. The following prompt is displayed:

Warning!! Deleting the array will render array unusable. Do you want to delete the array?(Yes/No):

RAID 1 only - the following prompt is also displayed:

Deleting the partition will result in data loss! Do you also want to delete the partition? (Yes/No):

6. Press **Yes** to delete the array or partition or **No** to return to the previous menu.
7. Press **Esc** to return to the previous menu.

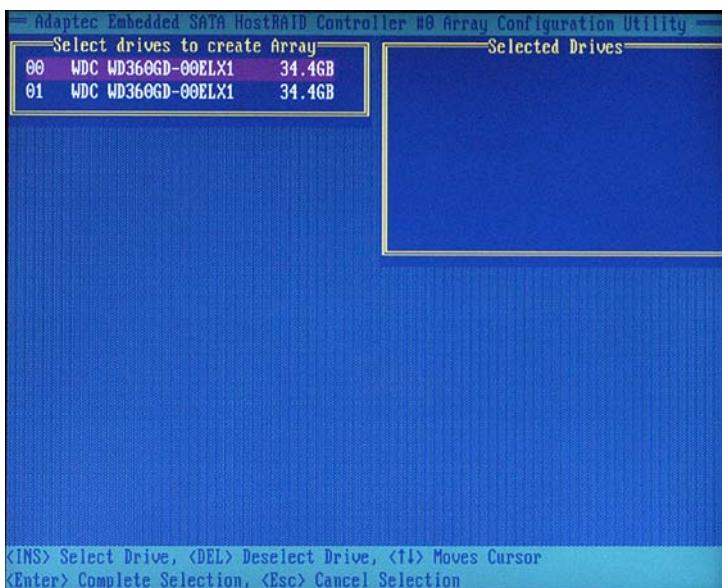
Creating Arrays

Before creating arrays, make sure the disks for the array are connected and installed in your system. Note that disks with no usable space, or disks that are un-initialized are shown in gray and cannot be used. See *Initializing Disk Drives*.

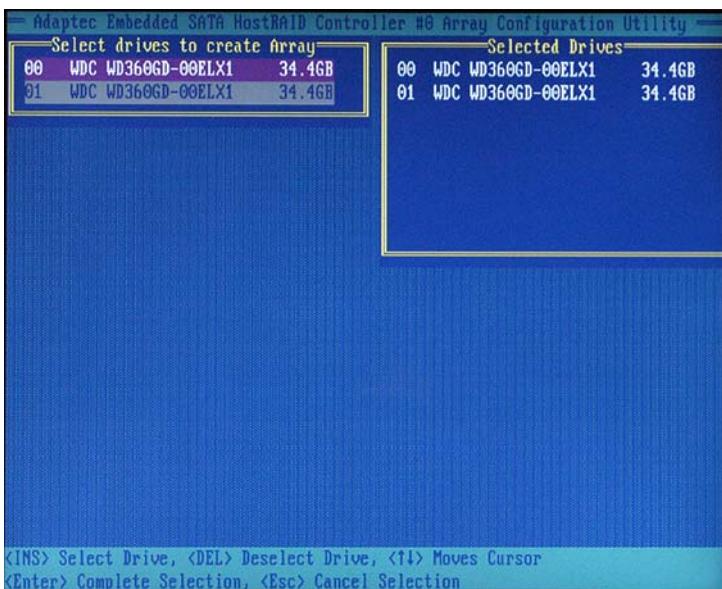
To create an array

1. Turn on your computer and press **Ctrl+A** when prompted to access the ARC utility.
2. From the ARC menu, select **Array Configuration Utility Main Menu (ACU)** (as shown on the first screen on page B-4).
3. From the ACU menu select **Create Array**.
4. Select the disks for the new array and press **Insert** (as shown on the screen below).

Note: To deselect any disk, highlight the disk and press **Delete**.



5. Press **Enter** when both disks for the new array are selected. The Array Properties menu displays (as shown on the screen on the next page).



Assigning Array Properties

Once you've create a new array, you are ready to assign properties to the array.

Caution: Once the array is created and its properties are assigned, you cannot change the array properties using the ACU. You will need to use the Adaptec Storage Manager - Browser Edition. (Refer to Adaptec's User's Guide in the enclosed CD.)

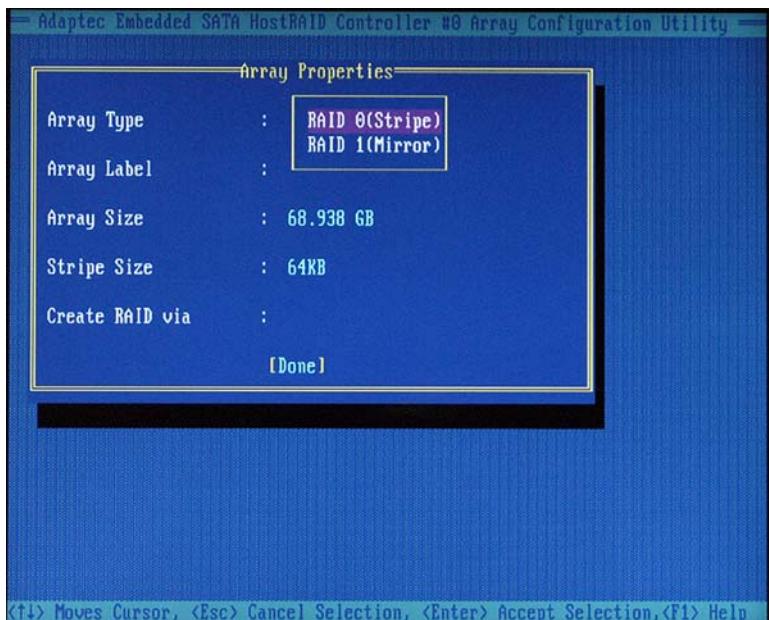
To assign properties to the new array

1. In the Array Properties menu (as shown on the following screen), select an array type and press **Enter**.

Note that only the available array types (RAID 0 and RAID 1) are displayed on the screen. (Using RAID 0 or RAID 1 requires two drives.)

2. Under the item "**Arrays Label**", type in a label and press **Enter**. The label cannot be more than 15 characters.

3. For RAID 0, select the desired stripe size. (Available stripe sizes are 16, 32, and 64 KB - default. It is recommended that you *do not* change the default setting.)



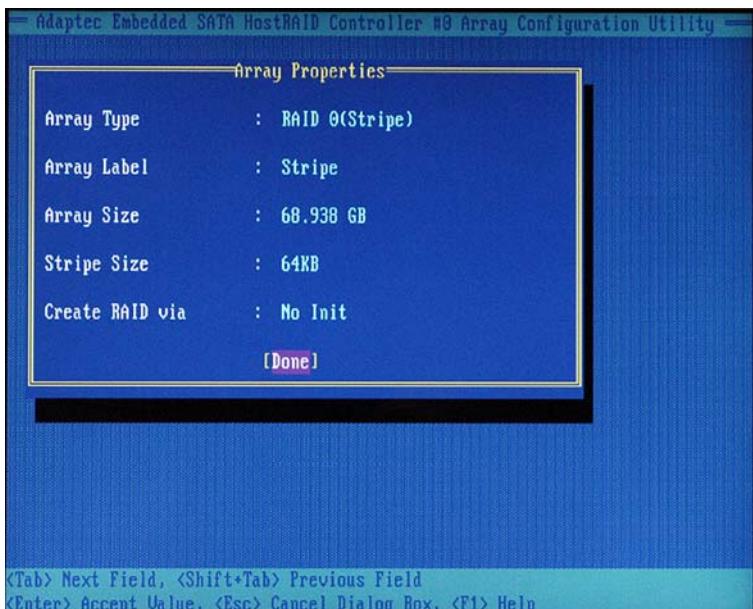
4. The item "Create RAID via" allows you to select between the different methods of creating RAID 0 and RAID 1.

The following table gives examples of when each is appropriate.

Raid Level	Create Via	When Appropriate
RAID 0	No Init	Creating a RAID 0 on new drives
RAID 0	Migrate (*Note)	Creating a RAID 0 from one new drive and one drive with data you wish to preserve
RAID 1	Build1	Any time you wish to create a RAID 1, but especially if you have data on one drive that you wish to preserve
RAID 1	Clear	Creating a RAID 1 on new drives, or when you want to ensure that the array contains no data after creation.
RAID 1	Quick	Fastest way to create a RAID 1. Appropriate when using new drives
RAID 1	Init	

5. When finished, press **Done** (as shown on the following screen).

Note: If you select Migrate for RAID 0, or Build for RAID 1, you will be asked to select the source drive. The contents of the source drive will be preserved. However, the data on the new drive will be lost.



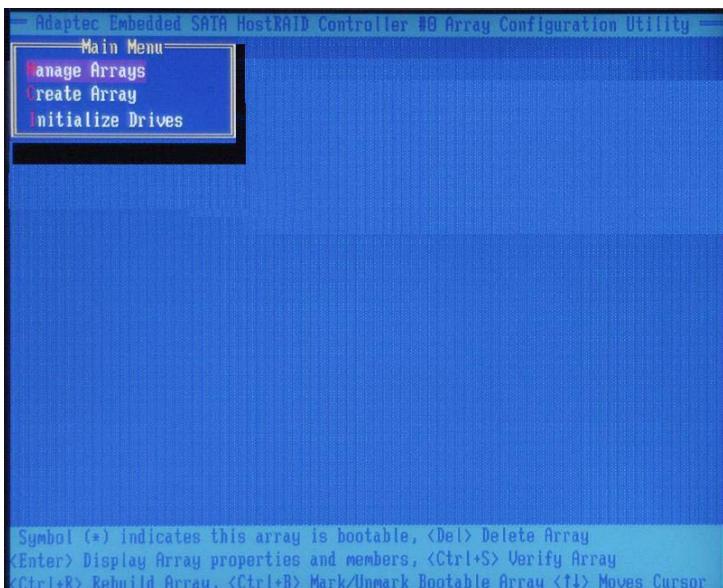
Notes

1. Before adding a new drive to an array, back up any data contained on the new drive. Otherwise, all data will be lost.
2. If you stop the build or clear process on a RAID 1 from ACU, you can restart it by pressing **Ctrl+R**.
3. A RAID 1 created using the Quick Init option may return some data mis-compare if you later run a consistency check. This is normal and is not a cause for concern.
4. The ACU allows you to use drives of different sizes in a RAID. However, during a build operation, only the smaller drive can be selected as the source or first drive.
5. When migrating from single volume to RAID 0, migrating from a larger drive to a smaller drive is allowed. However, the destination drive must be at least half the capacity of the source drive.
6. Adaptec does not recommend that you migrate or build an array on Windows dynamic disks (volumes), as it will result in data loss.

Warning: Do not interrupt the creation of RAID 0 using the Migrate option. If you do, you will not be able to restart or to recover the data that was on the source drive.

Adding a Bootable Array

1. From the Main menu, select **Manage Arrays**.
2. From the list of arrays, select the array you want to make bootable and press **Ctrl+B**.
3. Enter **Y** to create a bootable array when the following message is displayed: "This will make all other existing bootable array non-bootable. Do you want to make this array bootable? (Yes/No):" A bootable array will then be created. An asterisk will appear next to the bootable array.



Deleting a Bootable Array

To delete a bootable array

1. From the Main menu, select **Manage Arrays**.
2. From the List of Arrays, select the bootable array (*) you want to delete and press **Ctrl+B**. (A bootable array is an array marked with an asterisk.)
3. Enter **Y** to delete a bootable array when the following message is displayed: "The array is already marked bootable. Do you want to make this array as not bootable? (Yes/No):" The bootable array will then be deleted and the asterisk will disappear.

Note: do not use the delete key to delete a bootable array.

Adding/Deleting Hotspares

Note: In order to rebuild a RAID (RAID 0 or RAID 1), you need to add a new HDD as a hotspare.

1. Turn on your computer and press **Ctrl+A** as prompted to access the ARC Utility.
2. From the ARC menu, select **Array Configuration Utility** (ACU).
3. From the ACU menu, select **Add/Delete Hotspares**.
4. Use the up and down arrow keys to highlight and select the disk you want to designate as a hotspare, and press **Insert**, then press **Enter**.
5. Select **Yes** when the following prompt is displayed:

"Do you want to create spare?" (Yes/No?)

The spare you have selected will appear in the Select Drive Menu.

Initializing Disk Drives

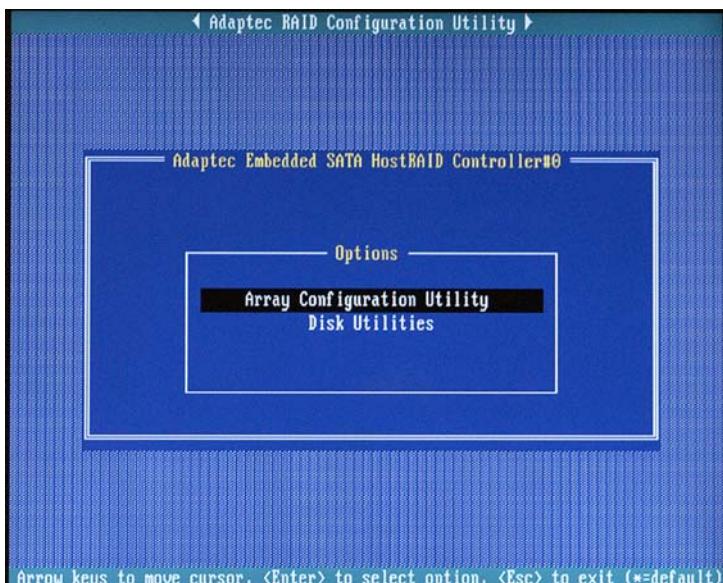
If an installed disk does not appear in the disk selection list for creating a new array or if it appears grayed out, you may have to initialize it before you can use it as part of an array. Drives attached to the controller must be initialized before they can be used in an array.

Caution: Initializing a disk overwrites the partition table on the disk and makes any data on the disk inaccessible. If the drive is used in an array, you may not be able to use the array again.

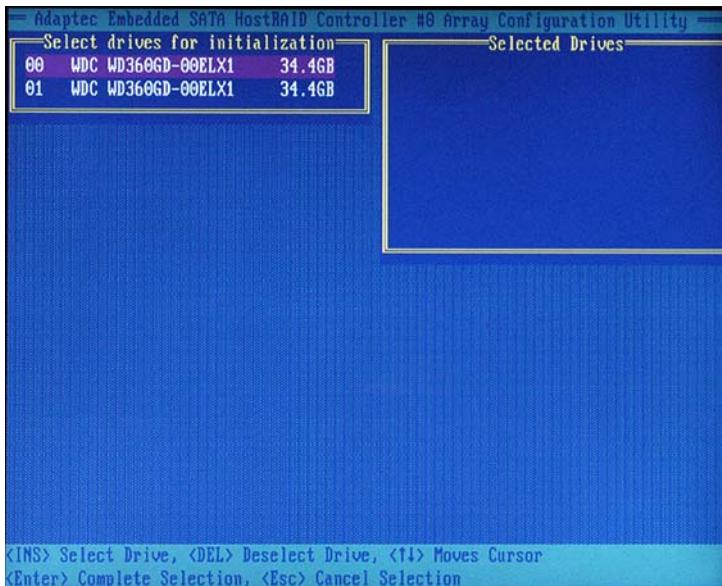
Do not initialize a disk that is part of a boot array. To determine which disks are associated with a particular array, please refer to *Viewing Array Properties*.

To initialize drives:

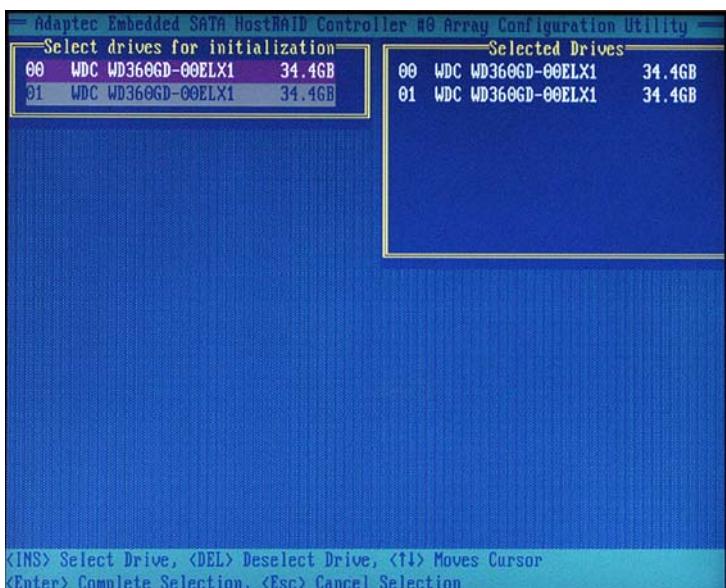
1. Turn on your computer and press **Ctrl+A** when prompted to access the ARC utility.
2. From the ARC menu, select **Array Configuration Utility (ACU)** (as shown on the screen below).



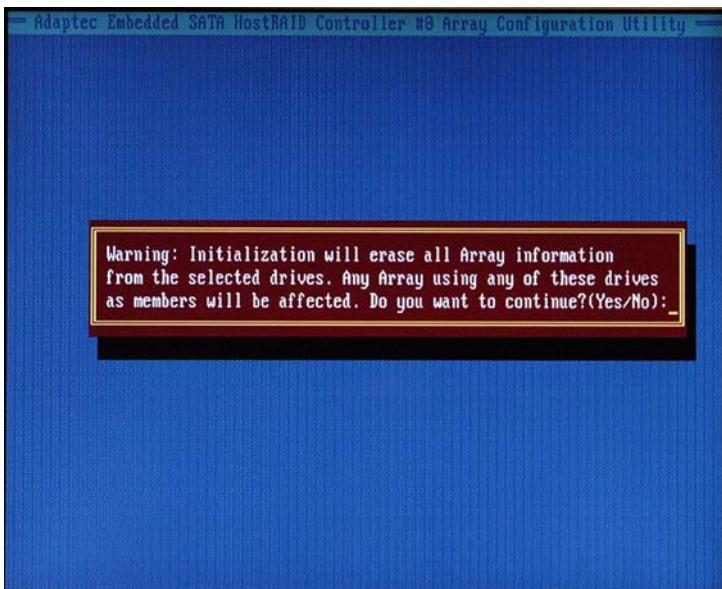
3. Select **Initialize Drives**.
4. Use the up and down arrow keys to highlight the disk you wish to initialize and press **Insert** (as shown on the following screen).



5. Repeat Step 4 so that both drives to be initialized are selected (as shown on the following screen).



6. Press **Enter**.
7. Read the warning message as shown on the screen below.



8. Make sure that you have selected the correct disk drives to initialize. If correct, type **Y** to continue.

Rebuilding Arrays

Note 1: Rebuilding applies to Fault Tolerant arrays (RAID 1) only.

If an array build process (or initialization) is interrupted or critical with one member missing, you must perform a rebuild to optimized its functionality. For a critical array rebuild operation, the optimal drive is the source drive.

Note 2: If no spare array exists and a hard disk drive fails, you need to create a spare before you can rebuild an array.

To Rebuild an array:

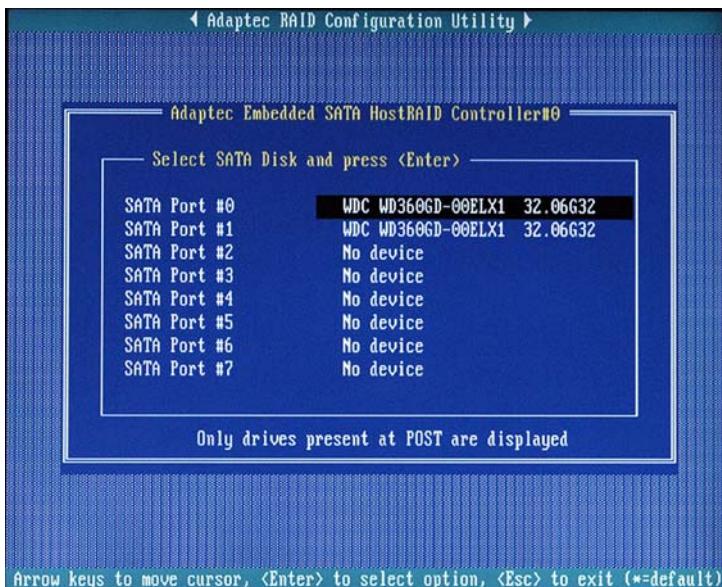
1. From the Main Menu, select **Manage Arrays**. From the list of arrays, select the array you want to rebuild.
2. Press **Ctrl+R** to rebuild.

Using the Disk Utilities

The Disk Utilities enable you to format or verify the media of your Serial ATA hard disks.

To access the disk utilities:

1. Turn on your computer and press **Ctrl+A** when prompted to access the ARC utility.
2. From the ARC menu, select **Disk Utilities**.
3. Select the desired disk and press **Enter** (as shown below.)



You can choose from the following options

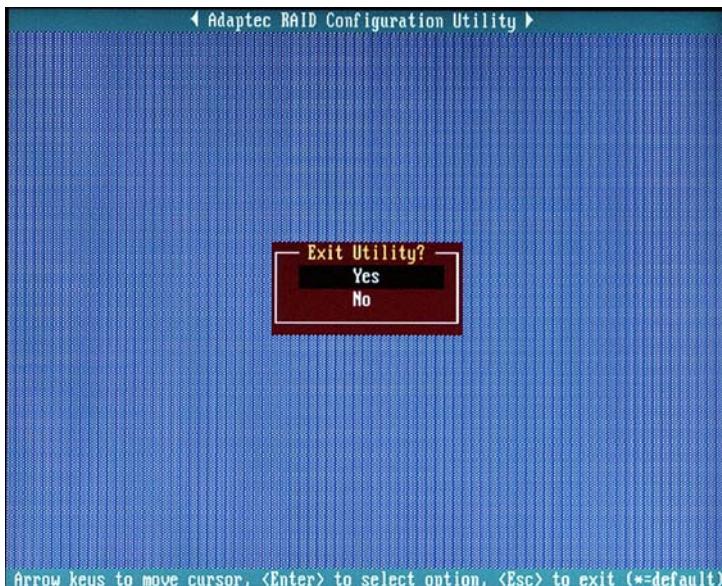
1. Format Disk - Simulates a low-level format of the hard drive by writing zeros to the entire disk. Serial ATA drives are low-level formatted at the factory and do not need to be low-level formatted again.

Caution: Formatting destroys all data on the drive. Be sure to back up your data before performing this operation.

2. Verify Disk Media - Scans the media of a disk drive for defects.

To Exit Adaptec RAID Configuration Utility

1. Once you have completed RAID array configurations, press **ESC** to exit. The following screen will appear.



2. Press **Yes** to exit the Utility.

For more information regarding the Adaptec RAID Utility, please refer to Adaptec's User's Guide in the CD included in your shipping package. You can also download a copy of Adaptec User's Guide from our web site at: www.supermicro.com.

B-2 Installing Marvell's Hercules II SATA Driver by Adaptec and Windows OS

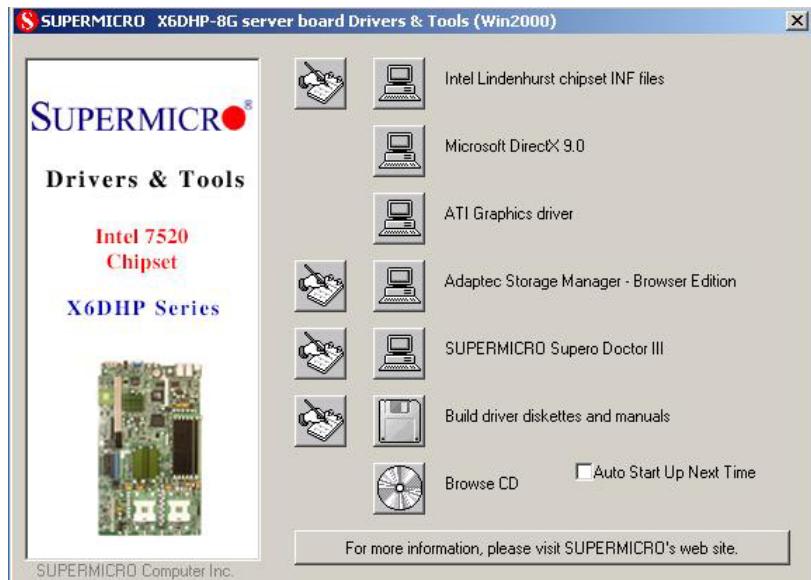
1. Insert Supermicro's bootable CD that came with the package into the CD Drive during the system reboot. The "Super Micro Driver Diskette Maker" screen will appear.
2. Choose "Marvell Hercules II SATA Driver by 3rd Party (Adaptec)" from the items listed and press **Enter**.
3. From the next screen displayed, choose the OS driver you want to install and press **Enter**.
4. Insert a formatted diskette into drive A: and press **Enter** as prompted.
5. Exit the program after the process is completed. Then, reboot the system.
6. Insert Microsoft Windows OS Setup CD in the CD Driver. The system will boot up from the CD.
7. Press the **F6** key when the message "Press F6 if you need to install a third party SCSI or RAID driver" is displayed.
8. When the Windows OS Setup screen appears, press **S** to specify additional device(s).
9. Insert the driver diskette labelled "Adaptec Embedded Serial ATA Raid Controller Driver" into your floppy drive and press the **Enter** key.
10. Choose **Adaptec Embedded Host Serial ATA Raid Controller** from the list indicated in the Windows OS Setup Screen and press the **Enter** key.
11. Press the **Enter** key to continue the installation process. (If you need to specify any additional devices to be installed, do so at this time.) Once all devices are specified, press the **Enter** key to continue with the installation.
12. From the Windows OS Setup screen, press the **Enter** key. The OS setup routine will automatically load all device files and continue the Windows OS installation.
13. After the Windows OS installation has completed, the system will automatically reboot.

B-3 Installing Other Software Programs and Drivers

Installing Other Drivers

After you've installed the Windows Operating System, a screen (as shown in Figure B-1) will appear. You are now ready to install additional software programs and drivers. To install these software programs and drivers, click the icons to the right of these items.

Figure B-1. Driver/Tool Installation Display Screen



Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** You should install everything here except for the SUPER Doctor utility, and the LAN/SCSI driver diskettes, which are optional. The bottom icon with a CD on it allows you to view the entire contents of the CD.

Note: Please refer to the Adaptec User's Guide (included in the CD) for installing the Adaptec SATA RAID Controller Driver. You can also download a copy of the guide from our web site.

Supero Doctor III

The Supero Doctor III program is a web-based management tool that offers both remote and local management tools. The local management application is called SD III Client. The Supero Doctor III program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. Supero Doctor III displays crucial system information such as CPU temperature, system voltages and fan status. See Figures B-2 and B-3 for examples of the Supero Doctor III interface.

Figure B-2. Supero Doctor III: Health Information Display

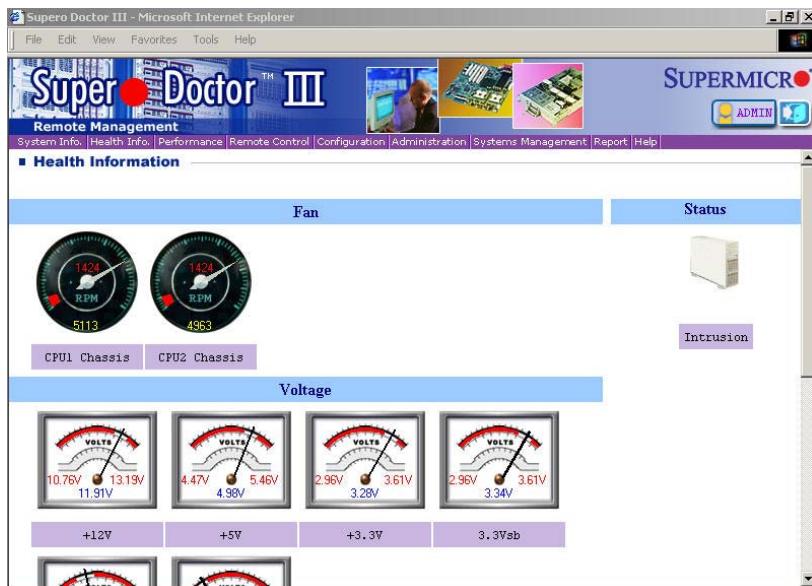


Figure B-3. Supero Doctor III: Remote Control Display



Graceful power control

Supero Doctor III allows a user to inform the OS to reboot or shut down within a specified time (the default is 30 seconds). Before the system reboots or shuts down, it's allowed to cancel the action.

Requirements

Keep Supero SD3Service Daemon running at all times on this system.
Provide TCP/IP connectivity.

Power control

Note: SD III Software Revision 1.0 can be downloaded from our Web site at: ftp://ftp.supermicro.com/utility/Supero_Doctor_III/. You can also download the SDIII User's Guide at: <http://www.supermicro.com/PRODUCT/Manuals/SDIII/UserGuide.pdf>. For Linux, we recommend using Supero Doctor II.